

Stepper Motor Microstepping with PIC18C452

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INTRODUCTION

A stepper motor, as its name suggests, moves one step at a time, unlike those conventional motors, which spin continuously. If we command a stepper motor to move some specific number of steps, it rotates incrementally that many number of steps and stops. Because of this basic nature of a stepper motor, it is widely used in low cost, open loop position control systems. Open loop control means no feedback information about the position is needed. This eliminates the need for expensive sensing and feedback devices, such as optical encoders. Motor position is known simply by keeping track of the number of input step pulses.

STEPPER MOTOR BASICS

Now let's take a closer look at a stepper motor. The first thing that we notice is that it has more than two wires leading into it. In fact, various versions have four, five, six, and sometimes more wires. Also, when we manually rotate the shaft, we get a 'notched' feeling. The simplest way to think about a stepper motor is as a bar magnet that pivots about its center with four individual, but exactly identical electromagnets, as shown in Figure 1A. If we manually rotate the magnet without energizing any coils, we get the 'notched' feeling whenever a relatively larger magnetic force is generated, because of the alignment of the permanent magnet with the core of the electromagnets, as in Figure 1A. This force is termed 'detent torque'. Let's assume that the initial position of the magnetic rotor is as shown in Figure 1A. Now turn on coil A; i.e., flow current through it to create an electromagnet, as shown in Figure 1B. The motor does not rotate, but we cannot move it freely by hand (more torque has to be applied to move it now), because of a larger 'holding torque'. This torque is generated by the attraction of the north and south poles of the rotor magnet and the electromagnet produced in the stator by the current.

FIGURE 1: NON-ENERGIZED AND CLOCKWISE CURRENT IN COIL A

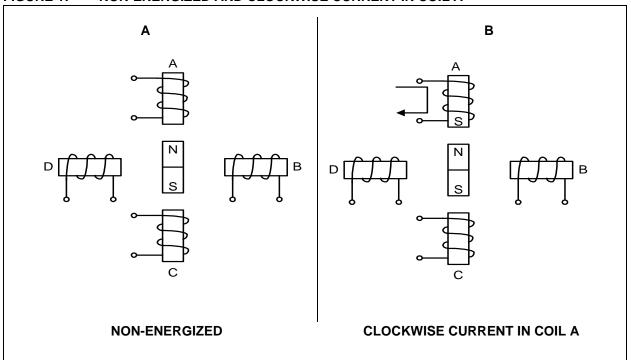
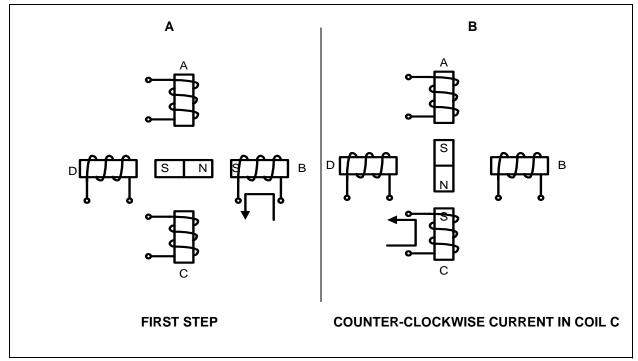


FIGURE 2: FIRST STEP MOVEMENT AND NEXT STEP



To move the motor in a clockwise direction from its initial stop position, we need to generate torque in the clockwise direction. This is done by turning off coil A, and turning on coil B. The electromagnet in coil B pulls the magnetized rotor and the rotor aligns itself with coil B, as shown in Figure 2A. Turning off coil B and turning on coil C will move the rotor one step further, as shown in Figure 2B.

Comparing Figure 1B and Figure 2B, we understand that the direction of current flow in coil C is exactly opposite to the direction of flow in coil A. This is required to generate an electromagnet of correct polarity, which will pull the rotor in the clockwise direction. By the same logic, the direction of current in coil D will be opposite to coil B when the rotor takes the next step (due to turning off coil C and turning on coil D).

A 360 degree rotation of the rotor will be completed if you turn off coil D and turn on coil A. The coil operation sequence (B, C, D, A), described is responsible for the clockwise rotation of the motor. The rotor will move counter-clockwise from its initial position at Figure 1B if we follow the opposite sequence (D, C, B, A).

UNIPOLAR AND BIPOLAR

Two leads on each of the four coils of a stepper motor can be brought out in different ways. All eight leads can be taken out of the motor separately. Alternatively, connecting A and C together, and B and D together, as shown in Figure 3, can form two coils. Leads of these two windings can be brought out of the motor in three different ways, as shown in Figure 3, Figure 4, and Figure 5.

If the coil ends are brought out as shown in Figure 3, then the motor is called a bipolar motor, and if the wires are brought out as shown in Figure 4 or Figure 5, with one or two center tap(s), it is called a unipolar motor.

FIGURE 3: BIPOLAR (4-WIRE)

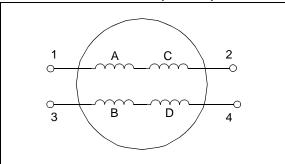


FIGURE 4: UNIPOLAR (5-WIRE)

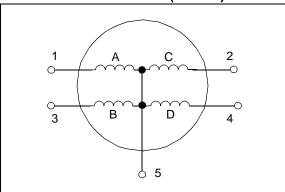
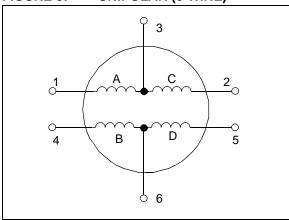


FIGURE 5: UNIPOLAR (6-WIRE)

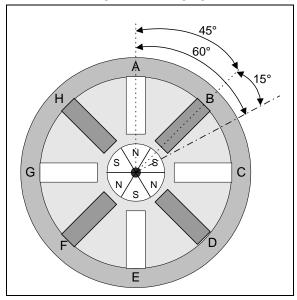


AN ACTUAL PERMANENT MAGNET (PM) STEPPER MOTOR

The simple stepper motor described, moves in very coarse steps of 90 degrees. How do actual motors achieve movements as low as 7.5 degrees? The stator (the stationary electromagnets) of a real motor has more segments on it. A typical stator arrangement with eight stators is shown in Figure 6.

FIGURE 6:

STATOR WINDING ARRANGEMENTS IN A PERMANENT MAGNET STEPPER MOTOR



The rotor is also different and a typical cylindrical rotor with 6 poles is shown in Figure 6. There are 45 degrees between each stator section and 60 degrees between each rotor pole. Using the principle of vernier mechanism, the actual movement of the rotor for each step is 60 minus 45 or 15 degrees. In this case, also, there are only two coils: one connects pole sections A, C, E and G, and the other connects B, D, F, H. Let us assume that current is flowing in a certain direction through the first coil only, and pole sections are wired in such a fashion that:

- · A and C have S-polarity
- · E and G have N-polarity

The rotor will be lined up accordingly, as shown in Figure 6. Let's say that we want the rotor to move 15 degrees clockwise. We would remove the current applied to the first winding and energize the second winding. The pole sections B, D, F, H are wired together with the second winding in such a way that:

- B and D have S-polarity
- F and H have N-polarity

In the next step, current through winding 2 is removed and reverse polarity current is applied in winding 1. This time A and C have N-polarity, and E and G have S-polarity; so the rotor will take a further 15 degree step in the clockwise direction. The principle of operation is the same as the basic stepper motor with a bar magnet as rotor and four individual electromagnets as stators. but in this construction, 15 degrees per step is achieved. Different 'step angles' (i.e., angular displacement in degrees per step) can be obtained by varying the design with different numbers of stators and rotor poles. In an actual motor, both rotor and stators are cylindrical, as shown in Figure 7. This type of motor is called a permanent magnet (PM) stepper because the rotor is a permanent magnet. These are low cost motors with typical step angles of 7.5 degrees to 15 degrees.

VARIABLE RELUCTANCE (VR) STEPPER MOTOR

There is a type of motor where the rotor is not cylindrical, but looks like bars with a number of teeth on it, as shown in Figure 8. The rotor teeth are made of soft iron. The electromagnet produced by activating stator coils in sequence, attracts the metal bar (rotor) towards the minimum reluctance path in the magnetic circuit. We don't get a notched feeling when we try to rotate it manually in the non-energized condition. In the non-energized condition, there is no magnetic flux in the air gap, as the stator is an electromagnet and the rotor is a piece of soft iron; hence, there is no detent torque. This type of stepper motor is called a variable reluctance stepper (VR). The motor shown in Figure 8 has four rotor teeth, 90 degrees apart and six stator poles, 60 degrees apart. So when the windings are energized in a reoccurring sequence of 2, 3, 1, and so on, the motor will rotate in a 30 degree step angle. These motors provide less holding torque at standstill compared to the PM type, but the dynamic torque characteristics are better.

Variable reluctance motors are normally constructed with three or five stator windings, as opposed to the two windings in the PM motors.

Permanent Magnet

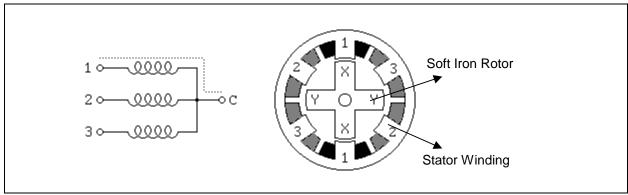
1a 0 0000 01b

2a 0 0000 02b

Stator Winding

FIGURE 7: A BIPOLAR PERMANENT MAGNET STEPPER MOTOR

FIGURE 8: A VARIABLE RELUCTANCE MOTOR



HYBRID (HB) STEPPER MOTOR

Construction of permanent magnet motors becomes very complex below 7.5 degrees step angles. Smaller step angles can be realized by combining the variable reluctance motor and the permanent magnet motor principles. Such motors are called hybrid motors (HB), which give much smaller step angles, as small as 0.9 degrees per step.

A typical hybrid motor is shown in Figure 9. The stator construction is similar to the permanent magnet motor, and the rotor is cylindrical and magnetized like the PM motor with multiple teeth like a VR motor. The teeth on the rotor provide a better path for the flux to flow through the preferred locations in the air gap. This increases the detent, holding, and dynamic torque characteristics of the motor compared to the other two types of motors.

Hybrid motors have a smaller step angle compared to the permanent magnet motor, but they are very expensive. In low cost applications, the step angle of a permanent magnet motor is divided into smaller angles using better control techniques.

Permanent magnet motors and hybrid motors are more popular than the variable reluctance motor, and since the stator construction of these motors is very similar, a common control circuit can easily drive both types of motors.

HOW TO IDENTIFY THE PERMANENT MAGNET/HYBRID MOTOR LEADS

The color code of the wires coming out of the motor are not standard; however, using a multimeter/ohmmeter, it is easy to identify the winding ends and center tap.

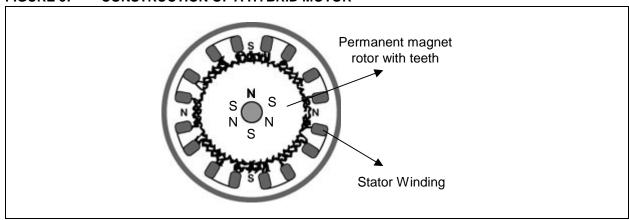
If only four leads are coming out of the motor, then the motor is a bipolar motor. If the resistance measured across two terminals, say terminals 1 and 2 in Figure 3, is finite, then those are ends of a coil. If the multimeter shows an open circuit (i.e., if you are trying to measure across the terminals 1 and 3, or 1 and 4, or 2 and 3, or 2 and 4), then the terminals are of different windings. Change your lead to another terminal and check again to find a finite resistance.

If there are five leads coming out of the motor, then the resistance across one terminal and all other terminals will be almost equal. This common terminal is the center tap and the other terminals are the ends of different windings. Figure 4 shows terminal 5 is the common terminal, while 1, 2, 3, and 4 are the ends of the windings.

In the case of a motor with six leads as in Figure 5, resistance across terminals 1 and 2 should be approximately double the resistance measured across terminals 1 and 3, and 2 and 3. The same is applicable for the other winding (the remaining 3 wires).

In all the above cases, once the terminals are identified, it is important to know the sequence in which the windings should be energized. This is done by energizing the terminals one after the other, by rated voltage. If the motor smoothly moves in a particular direction, say clockwise, when the windings are energized, then the energizing sequence is correct. If the motor hunts or moves in a jerky manner, then the sequence of winding segments has to be changed and checked again for smooth movement.

FIGURE 9: CONSTRUCTION OF A HYBRID MOTOR



TORQUE AND SPEED

The speed of a stepper motor depends on the rate at which you turn on and off the coils, and is termed the 'step-rate'. The maximum step-rate, and hence, the maximum speed, depends upon the inductance of the stator coils. Figure 10 shows the equivalent circuit of a stator winding and the relation between current rise and winding inductance. It takes a longer time to build the rated current in a winding with greater inductance compared to a winding with lesser inductance. So. when using a motor with higher winding inductance, sufficient time needs to be given for current to build up before the next step command is issued. If the time between two step commands is less than the current build-up time, it results in a 'slip', i.e., the motor misses a step. Unfortunately, the inductance of the winding is not well documented in most of the stepper motor data sheets. In general, for smaller motors, the inductance of the coil is much less than its resistance, and the time

constant is less. With a lower time constant, current rise in the coil will be faster, which enables a higher step-rate. Using a Resistance-Inductance (RL) drive can achieve a higher step rate in motors with higher inductance, which is discussed in the next section.

The best way to decide the maximum speed is by studying the torque vs. step-rate (expressed in pulse per second or pps) characteristics of a particular step-per motor (shown in Figure 11). 'Pull-in' torque is the maximum load torque that the motor can start or stop instantaneously without mis-stepping. 'Pull-out' torque is the torque available when the motor is continuously accelerated to the operating point. From the graph, we can conclude that for this particular motor, the 'maximum self-starting frequency' is 200 pps. The term 'maximum self-starting frequency' is the maximum step-rate at which the motor can start instantaneously at no-load without mis-stepping. While at no-load, this motor can be accelerated up to 275 pps.

FIGURE 10: MOTOR EQUIVALENT CIRCUIT AND CURRENT RISE RATE IN STATOR WINDING

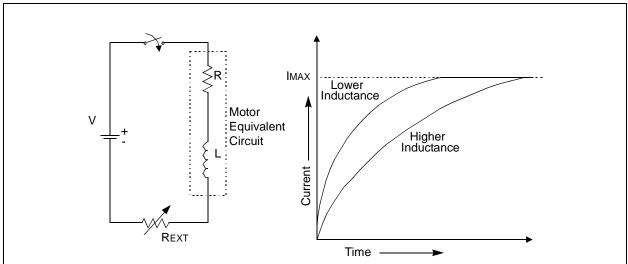
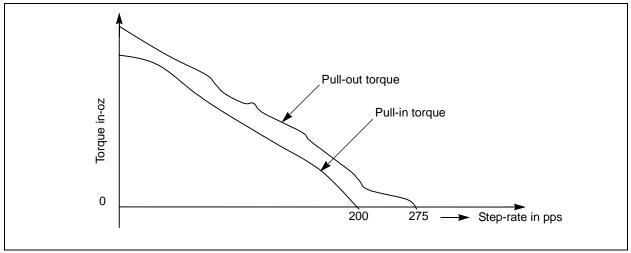


FIGURE 11: A TYPICAL SPEED VS. TORQUE CURVE



DRIVE CIRCUITS

The drive mechanism for 5-wire and 6-wire unipolar motors is fairly simple and is shown in Figure 12 (A and B). Only one coil is shown in this figure, but the other will be connected in the same way.

By comparing Figure 12A and Figure 12B, we see the direction of current flow is opposite in sections A and C of the coil, as per our explanation earlier. But the current flow in a particular section of the coil is always unidirectional, hence the name 'unipolar motor'.

Bipolar stepper motors do not have the center tap. That makes the motor construction easier, but it needs a different type of driver circuit, which reverses the current flow through the entire coil by alternating the polarity of the terminals, giving us the name 'bipolar'.

A bipolar motor is capable of higher torque since the entire coil is energized, not just half. Let's look at the mechanism for reversing the voltage across one of the coils, as shown in Figure 13.

This circuit is called an H-bridge, because it resembles a letter 'H'. The current can be reversed through the coil by closing the appropriate switches. If switches A and D are closed, then current flows in one direction, and if switches B and C are closed, then current flows in the opposite direction.

As the rating of the motor increases, the winding inductance also increases. This higher inductance results in a sluggish current rise in the windings, which limits the step-rate, as explained in the previous section. We can reduce the time constant by externally adding a suitable resistor in series with the coil and applying more than the rated voltage. The resistor should be chosen in such a way that the voltage across the coil does not exceed the rated voltage, and the additional voltage is dropped across the resistor. This method is also useful if we have a fixed power supply with an output of more than the rated coil-voltage specified. This type of drive is called a resistance-inductive (RL) drive. Electronic circuitry can be added to vary this resistor value dynamically to get the best result. The main disadvantage of this drive is that, since they are used with motors with large torque ratings, current flowing through the series resistor is large, resulting in higher heat dissipation and, hence, the size of the drive becomes bulky.

This resistor can be avoided by using PWM current control in the windings. In PWM control, current through the winding can be controlled by modulating the 'ON' time and 'OFF' time of the switches with PWM pulses, thus ensuring that only the required current flows through the coil, as shown in Figure 14.

FIGURE 12: SIMPLIFIED DRIVES FOR THE UNIPOLAR MOTOR

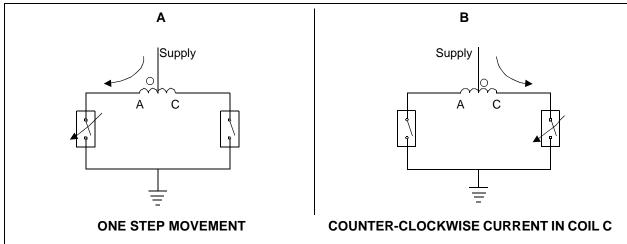


FIGURE 13: SIMPLIFIED H-BRIDGE CONFIGURATION

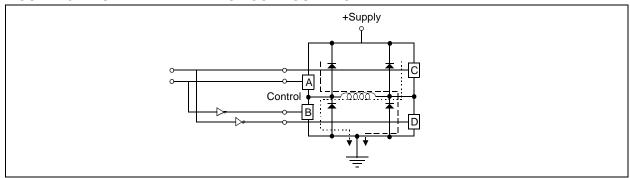
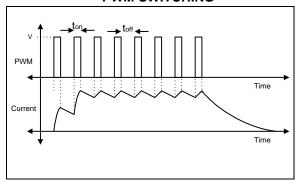


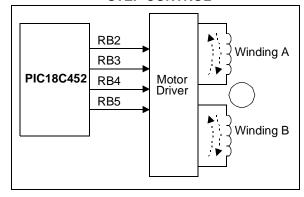
FIGURE 14: CURRENT WAVE FORM WITH PWM SWITCHING



STEPPER MOTOR CONTROL

To control a stepper motor, we need a proper driver circuit as discussed earlier. Unipolar drive can be used with unipolar motors only. In this application note, a bipolar drive is discussed, as this can be used to control both bipolar and unipolar motors. Unipolar motors can be connected to a bipolar driver by simply ignoring the center taps (by doing this, the motor becomes bipolar). Next we need a sequencer to issue proper signals in a required sequence to the H-bridges. A controller is built around the PIC18C452. Two H-bridges are used to control two windings of the stepper motors. Functional block diagram is shown in Figure 15. Example 1 shows the code required for full step control written for PIC18C452:

FIGURE 15: BLOCK DIAGRAM OF FULL STEP CONTROL



Code which configures PORTB<5:2> as output pins is not given in the example.

The code makes RB<5:2> outputs either '0' or '1' sequentially, which switches off or applies positive (+) or negative (-) polarity to Winding A and Winding B, as shown below:

Winding A	Winding B	
+	0	step 1
0	+	step 2
-	0	step 3
0	-	step 4

Legend:

- 0 = coil OFF
- += current flows in one direction
- -= current flows in the opposite direction

Note: Step 1 follows after step 4 and the cycle continues.

EXAMPLE 1: FULL STEP WITH 'ONE PHASE ON' AT A TIME

```
#define
        STEP ONE
                    b'00100000'
                               ; PortB<5:2> are used to connect the
        STEP TWO
#define
                    b'00010000'
                                 ; switches
        STEP THREE
                   b'00001000'
#define
#define
        STEP_FOUR
                    b'00000100'
  clrf
        STEP NUMBER
                                 ; Initialize start of step sequence
Initialize here TMR0 module, enable TMR0 interrupt and load a value in TMR0
 *****************
; Routine in TMR0 ISR which updates the current sequence for the next steps
       2000h
  ora
UPDATE STEP
  incf STEP_NUMBER,F
                                ; Increment step number
  btfsc STEP_NUMBER, 2
                                ; If Step number = 4h then clear the count
  clrf STEP_NUMBER
        STEP NUMBER, W
                                ; Load the step number to Working register
  movf
  call OUTPUT_STEP
                                 ; Load the sequence from the table
  movwf PORTB
                                 ; to Port B
  return
OUTPUT STEP
  addwf PCL, F
                                 ; Add Wreg content to PC and
  retlw STEP ONE
                                 ; return the corresponding sequence in Wreg
  retlw STEP_TWO
  retlw STEP_THREE
  retlw STEP FOUR
```

The step command sequence is updated in the Timer0 overflow Interrupt Service Routine. After issuing each step command in the sequence, PIC18C452 waits for the Timer 0 overflow interrupt to issue the next step sequence. This waiting time can be programmed by loading different values in the TMR0 register. Motor speed depends upon this value in the TMR0 register.

EQUATION 1: CALCULATE STEP COMMAND WAITING PERIOD

```
No. Steps per Revolution = 360/Motor Step Angle
pps = (rpm/60) * No. Steps per Revolution
Twait = 1/pps
```

For example, to turn a PM motor with a 7.5 degree step angle at a speed of 120 revolutions per minute (rpm), 96 pulses per second (pps) is required. This means that the waiting period should be 1/96 second to achieve this speed.

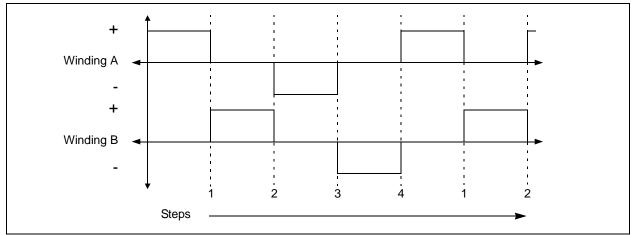
Instead of creating a software delay loop, Timer 0 module of PIC18C452 is loaded with an appropriate value to interrupt the processor every 1/96 second. Steps are updated in the Timer 0 Interrupt Service Routine. By loading different values in the Timer 0 module, the speed of the motor can be changed. The current through the two coils looks like a wave, as shown in Figure 16, so this is termed 'wave drive'.

This controller drives current through only one winding at a given time, so it is also termed 'One Phase On control'. This is the simplest kind of controller. The torque generated in this mode is less, as only one winding at a time is used. For the same stepper motor, we can improve the torque characteristics, by designing a better controller and thereby improving the drive capability.

The following are the most common drive types:

- · 'Two Phase On' full step drive
- Half step drive, where the motor moves half of the full step angle (7.5/2 degrees in the case of a motor with 7.5 degrees of step angle)
- Microstepping (which requires unequal current flow in two windings), where the rotor moves a fraction of the full step angle (1/4, 1/8, 1/16 or 1/32).





'TWO PHASE ON' FULL STEPPING

In this method, both windings of the motor are always energized. Instead of making one winding off and another on, in sequence, only the polarity of one winding at a time is changed as shown:

Winding A: + - - + + ...
Winding B: + + - - + ...

The code written for 'One Phase On' control is modified, as shown below in Example 2, to achieve 'Two Phase On' control.

The UPDATE_STEP function is the same as in Example 1, but in the OUTPUT_STEP function, two steps are AND'd (i.e., simultaneously two outputs of port B are '1'), which makes the two coils 'ON' simultaneously. The energizing sequence for both windings is shown in Figure 17.

EXAMPLE 2: 'TWO PHASE ON' CONTROL

```
#define STEP ONE
                       b'00100000'
                                    ; PortB<5:2> are used to connect the
#define STEP_TWO
                       b'00010000'
                                     ; switches
#define
         STEP THREE
                       b'00001000'
#define
         STEP FOUR
                        b'00000100'
        STEP_NUMBER
   clrf
                                     ; Initialize start of step sequence
   Initialize here TMR0 module, enable TMR0 interrupt and load a value in TMR0
; Routine in ISR which updates the current sequence for the next steps
org
        2000h
UPDATE STEP
   incf
        STEP NUMBER, F
                                    ; Increment step number
   btfsc STEP NUMBER, 2
                                     ; If Step number = 4h then clear the count
   clrf STEP NUMBER
   movf STEP NUMBER, W
                                     ; Load the step number to Working register
         OUTPUT STEP
   call
                                     ; Load the sequence from the table
   movwf PORTB
                                     ; to PortB
   return
OUTPUT STEP
                                     ; Add Wreg content to PC and
   addwf PCL,F
   retlw STEP ONE | STEP TWO
                                     ; return the corresponding sequence in Wreg
   retlw STEP TWO | STEP THREE
   retlw STEP_THREE | STEP_FOUR
   retlw STEP_FOUR | STEP_ONE
```

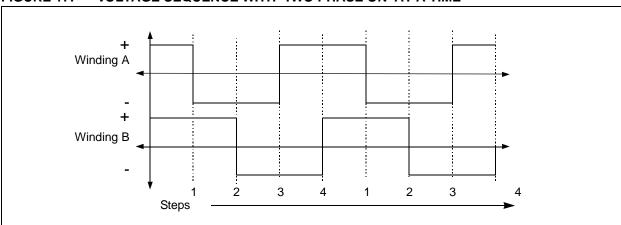
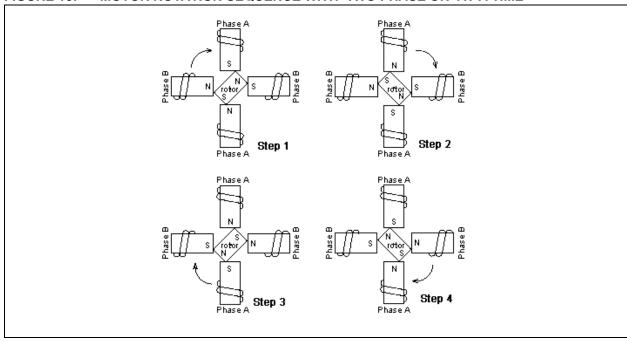


FIGURE 17: VOLTAGE SEQUENCE WITH 'TWO PHASE ON' AT A TIME





With the current flowing in both windings simultaneously, the rotor aligns itself between the 'average north' and 'average south' magnetic poles, as shown in Figure 18. Since both phases are always 'ON', this method gives 41.4 percent more torque than 'One Phase On' stepping.

One drawback of a stepper motor is that it has a natural resonant frequency. When the step-rate equals this frequency, we experience an audible change in the noise made by the motor, as well as an increase in vibration. The resonance point varies with the application and load, and typically occurs at low speed. In severe cases, the motor may lose steps at the resonant frequency. The best way to reduce the problem is to drive the motor in Half Step mode or Microstep mode.

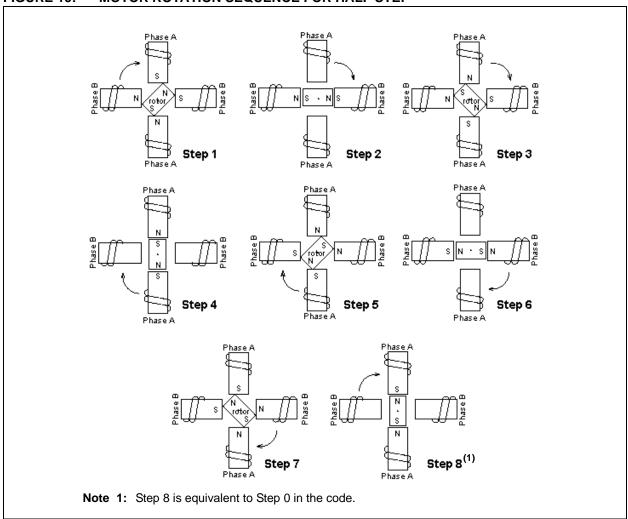
HALF STEPPING

This is actually a combination of 'One Phase On' and 'Two Phase On' full step control, as shown in Table 1.

TABLE 1: HALF STEP CONTROL

STEP_NUMBER	1	2	3	4	5	6	7	8 (0)
Rotor position	1/2	1	1½	2	2½	3	3½	4/0
Current in Winding A	+	0	-	-	-	0	+	+
Current in Winding B	+	+	+	0	-	-	-	0

FIGURE 19: MOTOR ROTATION SEQUENCE FOR HALF STEP



When current flows in only one winding, the rotor aligns with the stator poles in positions 0,1,2, and 3, as shown in Figure 19. When current flows in both windings, the rotor aligns itself between two stator poles in positions $\frac{1}{2}$, $\frac{1}{2}$, $\frac{2}{2}$, and $\frac{3}{2}$. So we see that, compared to a full step, the number of steps are doubled. This implies that a motor with a 7.5 degree step angle can be moved 3.75 degrees per step in Half Step mode and, hence,

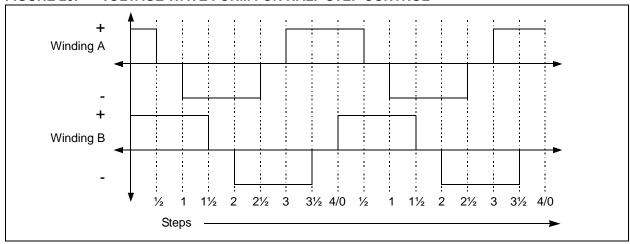
will take 96 steps to complete a rotation of 360 degrees, as compared to 48 steps in Full Step mode. Now, to rotate this motor at 120 rpm, as discussed earlier, the step-rate also has to be doubled to 192 pps.

The code to achieve half stepping is given in Example 3. The energizing sequence for the stator coils is shown in Figure 20.

EXAMPLE 3: HALF STEPPING

```
#define
         STEP ONE
                      b'00100000'
                                  ; PortB<5:2> are used to connect the
         STEP TWO
                                  ; switches
#define
                      b'00010000'
#define
         STEP THREE
                     b'00001000'
#define
         STEP FOUR
                      b'00000100'
   clrf
         STEP NUMBER
                                  ; Initialize start of step sequence
Initialize here TMR0 module, enable TMR0 interrupt and load a value in TMR0
 *****************
 *****
; Routine in ISR which updates the current sequence for the next steps
   org
UPDATE STEP
  Incf STEP_NUMBER,F
                                  ; Increment step number
  btfsc STEP_NUMBER, 3
                                  ; If Step number = 8h then clear the count
  clrf
        STEP NUMBER
   movf
        STEP NUMBER, W
                                  ; Load the step number to Working register
   call
         OUTPUT STEP
                                  ; Load the sequence from the table
  movwf PORTB
                                  ; to Port B
  return
OUTPUT STEP
  addwf PCL, F
                                  ; Add Wreg content to PC and
   retlw STEP_ONE
                                  ; return the corresponding sequence in Wreg
  retlw STEP_ONE | STEP_TWO
  retlw STEP_TWO
  retlw STEP_TWO | STEP_THREE
   retlw
        STEP_THREE
   retlw STEP THREE | STEP FOUR
   retlw STEP FOUR
   retlw STEP FOUR | STEP ONE
```

FIGURE 20: VOLTAGE WAVE FORM FOR HALF STEP CONTROL



MICROSTEPPING

During our earlier discussion, we have mentioned that halfstepping and microstepping reduces the stepper motor's resonance problem. Although the resonance frequency depends upon the load connected to the rotor, it typically occurs at a low step-rate. We have already seen that the step-rate doubles in Half Step mode compared to Full Step mode. If we move the motor in microsteps, i.e., a fraction of a full step (1/4, 1/8, 1/16 or 1/32), then the step-rate has to be increased by a corresponding factor (4, 8, 16 or 32) for the same rpm. This further improves the stepper performance at very low rpm. Moreover, microstepping offers other advantages as well:

- Smooth movement at low speeds
- Increased step positioning resolution, as a result of a smaller step angle
- Maximum torque at both low and high step-rates

But microstepping requires more processing power. If we study the flow diagrams for current (as shown for full or half steps), we conclude that the value of current in a particular coil is either 'no current' or 'a rated current'. However, in microstepping, the magnitude of current varies in the windings.

The function of a microstepping controller is to control the magnitude of current in both coils in the proper sequence.

THEORY OF MICROSTEPPING

The current flow diagrams, as well as the sequence of operations in case of full or half stepping, reveals that the electrical sequence repeats itself after every fourth full step. This phenomenon of stepper motor signifies that one full 'electrical cycle' consists of four full steps. Please note that one full 'electrical cycle' (i.e., 360 degrees of 'electrical angle') is different from one full revolution of the rotor (360 degrees of mechanical rotation). One full 'electrical cycle' always consists of four full steps. Hence, one full step of any stepper motor with any 'step angle' corresponds to 360/4 or 90 degrees of 'electrical angle'. If this 'electrical angle' is divided into smaller, equal angles, and a corresponding current is given to the stator windings, then it will look like Figure 21. So we can vary current in one winding with a sine function of an angle ' θ ' and in the other winding with a cosine function of ' θ '.

In a stepper motor, the rotor stable positions are in synchronization with the stator flux. When the windings are energized, each of the windings will produce a flux in the air gap proportional to the current in that winding. So the flux in the air gap is directly proportional to the vector sum of the winding currents, in the resultant vector direction. In Full Step and Half Step modes, rated current is supplied to the windings, which rotates the resultant flux in the air gap in 90 degrees and 45 degrees electrical, respectively, with each change in sequence. In microstepping, the current is changed in the windings in fractions of rated current. Therefore, the resultant direction of flux changes in fractions of 90 degrees electrical. Usually, a full step is further divided into 4/8/16/32 steps. (A step length shorter than 1/32 of a full step normally does not make any further improvement in the motion.)

To achieve the required rotating flux, you can calculate the magnitude of the current in the windings with the following formula:

EQUATION 2: FLUX FORMULA

 $Ia = IPEAK * sin\theta$ $Ib = IPEAK * cos\theta$

Where:

la = instantaneous current in stator winding A
 lb = instantaneous current in stator winding B
 θ = angle in electrical degrees from a full step

position (OR microstep angle)

IPEAK = rated current of winding

With the above equations, the resultant stator current is the vector sum of the individual winding currents.

=
$$\sqrt{((\text{IPEAK} * \sin \theta)^2 + (\text{IPEAK} * \cos \theta)^2)}$$

= IPEAK * $\sqrt{(\sin \theta^2 + \cos \theta^2)}$ = IPEAK $\angle \theta$ electrical degree

This shows that at any angle θ , the resultant current remains same and equal to 'IPEAK'.

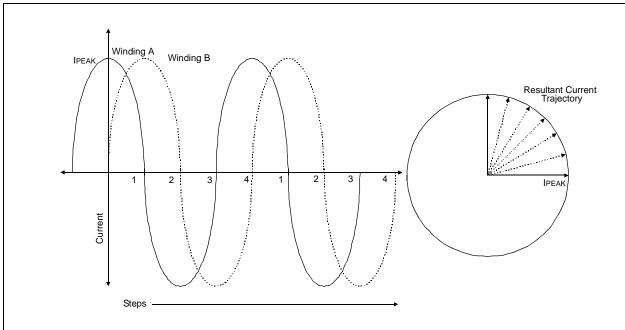


FIGURE 21: CURRENTS IN STATOR DURING MICROSTEP AND THE RESULTANT CURRENT

As shown in Figure 21, current in each winding will vary resulting in a rotating flux corresponding to IPEAK in the air gap. So for each increment of electrical angle θ , a flux and a torque corresponding to IPEAK is produced at an angle θ , thus producing a constant rotating flux/torque, which makes microstepping possible.

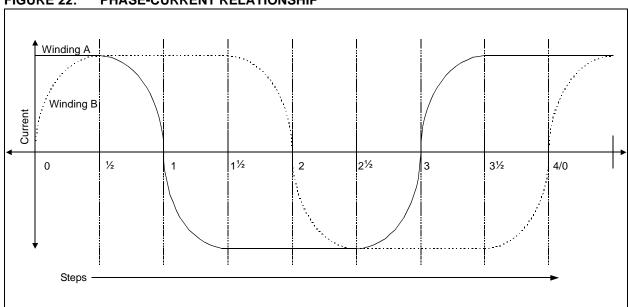
But in practice, the current in one winding is kept constant over half of the complete step and current in the other winding is varied as a function of $\sin\theta$ to maximize the motor torque, as shown in Figure 22.

Thus, the resultant current is:

$$= \sqrt{((\text{IPEAK})^2 + (\text{IPEAK} * \sin\theta)^2)}$$

$$= |\text{IPEAK} * \sqrt{(1 + \sin\theta^2)} \ge |\text{IPEAK} \angle \theta \text{ electrical degrees}$$





IMPLEMENTATION

The question is how to drive variable currents through the coil connected to a single supply source. There are different ways to achieve this, but the best way is:

- Connect one voltage source across the H-bridge so that when one pair of opposite switches are on, rated voltage is applied to the stator coil.
- 2. Vary the PWM duty cycle to control current through the coil.

The controller is built around the PIC18C452 microcontroller. A block diagram is shown in Figure 23. An actual circuit schematic is given in Appendix A. Two PWM modules of PIC18C452 are used to control current through two windings of the stator, and can be used for both full or half step.

Added features in the controller are:

- Speed setting through a potentiometer connected to one of the ADC channels of the PIC18C452.
- A step switch connected to one of the inputs of PORTB. If this switch is pressed, then the motor moves only one step (full, half or microstep).
- A toggle switch connected to one of the inputs to PORTB that decides the direction: forward or reverse.
- A DIP switch, connected to PORTD, is used to select the number of microsteps.
- DIP4 is used as the "Enable" switch. This has to be closed to run the motor with microsteps selected by DIP1-3.

Details of the DIP switches are shown in Table 2.

TABLE 2: DIP SWITCHES

No. of Steps	SW4 (RD5)	SW3 (RD2)	SW2 (RD1)	SW1 (RD0)
Full Step	Close	Open	Open	Close
Half Step	Close	Open	Close	Open
4	Close	Open	Close	Close
8	Close	Close	Open	Open
16	Close	Close	Open	Close
32	Close	Close	Close	Open

Note: Invalid where switches are all open or all closed.

Theoretically, the number of microsteps can be even more than 32, but practically, that does not improve stepper performance. The motor can be driven in microsteps by changing the currents in both windings, as a function of sine and cosine, simultaneously. Alternatively, the current is kept constant in one winding, while it is varied in the other, as shown in Figure 24. In practice, the second method is followed to maximize torque. Theoretically, the variation follows a sine curve, but may vary slightly for different motors to get improved step accuracy.

Appropriate values of the PWM duty cycle (proportional to the required coil current) for each step are given in Appendix B. A table corresponding to the PWM duty cycle is stored in the program memory of PIC18C452. The Table Pointer (TBLRD instruction) of PIC18C452 is used to retrieve the value from the table and load it to the PWM registers to generate an accurate duty cycle.

The assembly code to realize the microstepping is given in Appendix C.

The serial interface with a host computer is done using an USART module on the PIC18C452.

On the Host PC side, "Hyper Terminal" is used for communication. The serial link parameters are:

Baud rate: 9600

Data bits: 8

Parity: none

Stop bit: 1

Flow control: none

The commands shown in Table 3 can be set and run from the host PC.

Memory Usage

On-chip ROM used: 3580 bytes On-chip RAM used: 26 bytes

CONCLUSION

Microstepping a stepper motor increases stepping accuracy and reduces resonance in the motor. The two PWMs in the PIC18C452 can be used to control the voltage to the windings of a bipolar stepper motor.

A sine lookup table is entered in the program memory and accessed using the table read instructions. An on-chip USART communicates with the host PC for control parameters, and motor speed can be set using a potentiometer connected to one of the ADC channels.

TABLE 3: HOST PC COMMANDS

Command	Description	Range	Remarks/Data Value
0	Exit from PC interface	_	Control goes to the parameters set on the Reference board, like pot., FWD/REV switch, DIP switch
1	Number of microsteps	1 to 6	 Full step Half step 1/4 step 1/8 step 1/16 step 1/32 step
2	Direction of rotation	0 to 1	0 = Forward 1 = Reverse
3	Number of steps to inch	1 to 999	Inches in the selected direction and by selected step length
4	RPM	1 to 200	Rotates at set RPM, in set direction

FIGURE 23: BLOCK DIAGRAM OF CIRCUIT FOR MICROSTEPPING

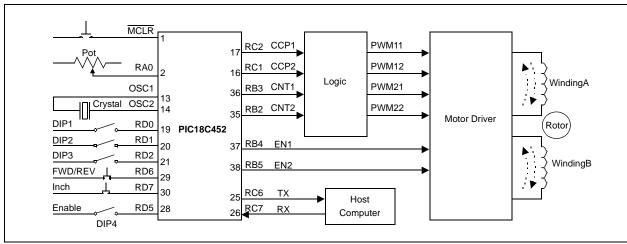
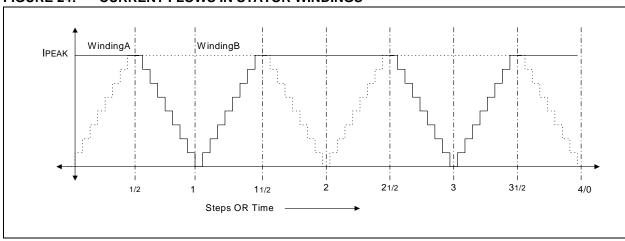


FIGURE 24: CURRENT FLOWS IN STATOR WINDINGS



APPENDIX A: SCHEMATIC DETAILS

The control scheme uses PIC18C452 for control and a driver IC, which has two H-bridges for driving the motor.

- Four PWMs required are derived from two CCPs (CCP1 and CCP2 in PWM mode). Control signals CNT1 and CNT2 switches CCP1 and CCP2 to appropriate PWM inputs of Driver IC (U2 and U5). CNT1 and CNT2 are connected to RB3 (Pin 36) and RB2 (Pin 35) of microcontroller (U1), respectively.
- EN1 and EN2 signals enable two sets of bridges in the driver IC (only for U2), connected to RB4 (Pin 37) and RB5 (Pin 38) of U1, respectively.
- Current feedbacks from the motor windings are converted to voltages by resistors R9 and R10, connected to Pin 1 and 15 of U2. These feedbacks are connected to AN1 (Pin 3) and AN3 (Pin5).
- I/O pin RD5 (Pin 28) is connected with a SPST switch for drive enable.
- I/O pin RD6 (Pin 29) is connected to a push-button switch for motor direction selection (FWD/REV). Each press of the switch will toggle the direction.
- I/O pin RD7 (Pin 30) is connected to a push-button switch for "Inch" movement of the motor. Each press of this switch will move the motor by a step, controlled by software.
- DIP switches connected to PORT<2:0> select the number of steps, as explained in the previous section.
- A 20 MHz crystal is used as the main oscillator.

FIGURE A-1: CIRCUIT DIAGRAM (SHEET 1 OF 2) +5V U1 11 VDD +5V RE2 32 _{VDD} RE1 RE0 8 C1 C2 RD7 30 MCLR **MCLR** .1 μF .1 μF 29 ≷R1 ≷4.7k RD6 SW1 RA0 RA0 RD5 3 RA1 RD4 27 AN1 <u>+</u>5V RD3 22 RD2 21 4 RA2 D1 1N914 5 RA3 AN3 RD2 6 7 RA5 RD1 20 СЗ RD1 RD0 19 R2 ≤ 10k ≤ RD0 33 RB0 RC7 34 RB1 RC6 RC5 24 35 RB2 CNT2 RC4 23 36 RB3 CNT1 37 RB4 RC3 18 EN1 RC2 17 CCP1 RC1 16 (CCP2) 38 RB5 (EN2 39 RB6 RC0 15 40 RB7 OSC2 Υ1 12 Vss OSC1 31 Vss +5V \$R5 \$2 2' C5_ $\begin{cases} R3 & R4 \\ 2.2k & 2.2k \end{cases}$ ₹R6 2.2k PIC18C452 27 pF 27 pF ².2k SW4 RD0 RD1 RD2 RD5 +5V ₹ R8 2.2k SW2 2.2k RD7 Inch RD6 Fwd/Rev VR1 CN1 LM340T-5.0 □-OUT +5V 0 C15 C10 COM C11 100 μF .1 μF .1 μF CN2 R13 470 VS □− 0

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FIGURE A-2: CIRCUIT DIAGRAM (SHEET 2 OF 2) +5V ٧S C8 C9 .1 μF D2 D3 D4 D5 .1 µF U3:A CN3A CCP1 IN1 > GND PWM1 OUT Ηп W1/1 CNT1 74HC08 W1/2 PWM2 OUT2 IN2 4 U3:B U2⁽¹⁾ U4:A 13 PWM3> IN3 OUT3 W2/1 0 W2/2 74HC08 PWM4> 12 74HC04 OUT4 10 U3:C CCP2 CN3B SENSEB BNA ANA SENSEA EN1 CNT2 D6 D7 D8 D9 74HC08 EN2 L298 1 15 U3:D U4:B R11_{WM} 2.2k AN1 74HC08 R12_W2.2k 74HC04 R10 ∫1.2Ω (AN3 R9 C7 C6 1.2Ω ≶ .1 μF +5V .1 µF VS C13 C12 .1 μF .1 μF C16 C14 U5⁽¹⁾ 1.0 μF .1 μF Vcc 1A FILM For U3 and U4 2 13 1B PWM1 1Y W1/1 3 2A 4 12 W1/2 U4:C U4:E PWM2 2B 2Y 5 ЗА 6 PWM3 3В 3Y W2/1 8 U4:D U4:F 4A 9 PWM4 4Y 4B W2/2 GND 74HC04 74HC04 TC4469 (DIP) P3)PIN1 PIN6 U6)PIN2 PIN7 11IN T1OUT TX) PIN3 PIN8 10 12IN T2OUT 13 PIN4 $\langle RX \rangle$ R10UT R1N PIN9 8 R2OUT R2N OPIN5 C1+ C1-16 Vcc +5V C2+ C2-V+ OE95-FRS 15 GND C30 C31 C34 TC232 C32 C33 .1 μF .1 µF .1 μF .1 μF .1 μF +5V Note 1: Vs range for U2 and U5: - If TC4469 is used a) 4.5V to 18V b) 2V to 46V - If L298 is used 2: Output current rating for circuit: a) 250 mA/winding - If TC4469 is used - If L298 is used b) 2A/winding

APPENDIX B: PWM DUTY CYCLE VALUES

TABLE B-1: TRUTH TABLE FOR FULL STEP OF A STEPPER MOTOR (BIPOLAR MOTOR)

Step Number		Current in Winding 2	PWM1 Duty Cycle CCP1	PWM2 Duty Cycle CCP2	EN1 RB4	EN2 RB5	CNT1 RB3	CNT2 RB2	PORTB Value
0	+1	0	100%	0%	Н	L	Н	L	0x18
1	0	+1	0%	100%	L	Н	L	Н	0x24
2	-1	0	100%	0%	Н	L	L	L	0x10
3	0	-1	0%	100%	L	Н	L	L	0x20

TABLE B-2: TRUTH TABLE FOR MICRO-STEP OF A STEPPER MOTOR (BIPOLAR MOTOR)

Step Number		Current in	Current in	PWM1 Duty	PWM2 Duty	EN1	EN2	CNT1 RB3		CNT2 RB2		PORTB Value	
Step Range	Micro Step	Winding 1	Winding 2	Cycle CCP1	Cycle CCP2	RB4	RB5	FWD	REV	FWD	REV	FWD	REV
0 to Half Section 2.1	0	+1	+ Sin 5.6°	100%	9.8%	Н	Н	Н	Н	Н	L	0x3C	0x38
	1	+1	+ Sin11.25°	100%	20%	Н	Н	Н	Н	Н	L	0x3C	0x38
	2	+1	+ Sin 16.8°	100%	29%	Н	Н	Н	Н	Н	L	0x3C	0x38
	3	+1	+ Sin 22.5°	100%	38%	Н	Н	Н	Н	Н	L	0x3C	0x38
	4	+1	+ Sin 28°	100%	47%	Н	Н	Н	Н	Н	L	0x3C	0x38
	5	+1	+ Sin 33.75°	100%	56%	Н	Н	Н	Н	Н	L	0x3C	0x38
	6	+1	+ Sin 39°	100%	63%	Н	Н	Н	Н	Н	L	0x3C	0x38
	7	+1	+ Sin 45°	100%	71%	Н	Н	Н	Н	Н	L	0x3C	0x38
	8	+1	+ Sin 50.6°	100%	77%	Н	Н	Н	Н	Н	L	0x3C	0x38
	9	+1	+Sin 56.25°	100%	83%	Н	Н	Н	Н	Н	L	0x3C	0x38
	10	+1	+ Sin 61.8°	100%	88%	Н	Н	Н	Н	Н	L	0x3C	0x38
	11	+1	+ Sin 67.5°	100%	93%	Н	Н	Н	Н	Н	L	0x3C	0x38
	12	+1	+ Sin 73.1°	100%	95.6%	Н	Н	Н	Η	Н	L	0x3C	0x38
	13	+1	+Sin 78.75°	100%	98%	Н	Н	Н	Н	Н	L	0x3C	0x38
	14	+1	+ Sin 84.35°	100%	99.5%	Н	Н	Н	Η	Н	L	0x3C	0x38
	15	+1	+ Sin 90°	100%	100%	Н	Н	Н	Н	Н	L	0x3C	0x38

Note 1: Current is in one winding constant for a half of the full step and current in other winding varying sinusoidal.

^{2:} Table is direct for 32 microsteps/step.

^{3:} For -16, -8, -4, -2 (half step); 2,4, and 8 microsteps are skipped, respectively, from this table.

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APPENDIX C: ASSEMBLY CODE FOR MICROSTEPPING

```
; PROGRAM :
                     STEPPER MOTOR CONTROL
; MICROCONTROLLER :
                      18C452
; CRYSTAL FREQUENCY : 20MHz
;DRIVER IC USED : TC4469/ST's L298
;Documents to be referred with this :
; a) Diagram of control circuit
; b) Application note: Microstepping of stepper motor using 18CXXX
           Padmaraja Yedamale , IDC
; AUTHOR :
;DATE :
:Version :
;Description:-
;This module controls Stepper motor in Full steps, Half steps and
;microsteps of -4,-8,-16,-32 per full step.
;TimerO is used for Speed control, which is rate of change of steps.
; Speed of the motor is varied by a potentiometer connected to the
;ADC channel0, which is loaded to TMR0.
;Direction of motor rotation can be changed using the Tact switch(FWD/REV)
; connected to PORTD<6>(Pin29). An internal buffer toggles and changes the
;direction with each press.
;Motor can be "Inched"(i.e. moved in steps) by using the switch(INCH)
; connected to the PORTD<7>(Pin30). Each press of this switch will move
;the motor by one step(full, half or the selected microstep), in the
;selected direction of FWD/REV.
;The DIP swithes DIP1(PORTD<0>,Pin19),DIP2(PORTD<1>,Pin20),DIP3(PORTD<0>,Pin21)
; are used to select number of steps as shown in the following table
     Sl no. No. of Steps DIP3(RD2) DIP2(RD1) DIP1(RD0)

1 Full step(1) Open Open Close

2 Half step(2) Open Close Open
                                              Close
     3
                                Open
                                                            Close
                4
               8
                                Close
                                              Open
                                                            Open
               16
                                Close
                                              Open
                                                            Close
               32
                                Close
                                              Close
;------
;DIP4 connected to PORT<5>,pin 28 is used as "Control enable" switch.
; If this is open, motor is inhibited from rotating.
; This module uses CCPx's in PWM mode
; In this module current in one of the winding is kept constant (rated)
; over half of the complete step and current in the other winding
; is varied sinusoidally, in order to maximize the rotor torque.
;Resultant rotor Torque = sqrt(1 + (Sine(angle)*Sine(angle))
;which is always > 1
```

```
; A table with PWM values is stored in the program memory. Table pointers and
; Table access instrucions are used to read the table as required for microstepping.
;An interface with host computer is given through serial port. USART module in the
;PIC18Cxxx is used for the communication. Following commands are implemented.
;Command Explanation Data value
                                      Range
;------
; 0 Exit from PC interface
                                                  Control goes to the
                                                  parameters set on the
                                                  Reference board, like pot.,
                                                  FWD/REV switch, DIP switch
;------
                            1-Full step 1 to 6
       No. of microsteps
                            2-Half step
                            3-1/4 step
                             4-1/8 step
                             5-1/16 step
                             6-1/32 step
; 2 Direction of rotation
                            0-Forward
                                     0 to 1
                            1- Reverse
;------
; 3 No. of steps to Inch
                                      1 to 999
                                                 Inches in the selected
                                                  direction and by selected
                                                 step length
;------
; 4 RPM
                                      1 to 200
                                                Rotates at set RPM in set
                                                  direction
<p18c452.inc>
;Variables definition
; **********************
   UDATA ACS
                        ;Relocatable variables in access RAM
STEP NUMBER
             res 1 ; Used for tracking the microstep counts
MOTOR DIRECTION res 1 ; Motor direction byte
                         ;0 indicates Reverse rotation
                        ;1 indicates forward
             res 1
res 1
res 1
                        ;Counter used for counting key debounce time
COUNTER
COUNTER1
                         ;Counter used for counting key debounce time
SPEED REF H
              res
                    1
                         ;Speed referance, read from ADCO, connected
              res 1
SPEED REF L
                        ;to Preset on the board
FLAG BYTE
              res 1
                        ;Indicates status flags
STEP JUMP
              res 1 ;Step jump count based on DIP switch setting
RECIEVED BYTE
             res 1 ;Byte recieved from host PC
COMMAND_BYTE
                    1 ;Command from host PC
             res
INCH VALUE
                    2
                        ;Inch count from host PC
             res
                        ;RPM value
RPM VALUE
             res
                    4
            res
                        ; No. of microsteps stored
MICRO STEPS
                    1
                    3
                         ;Temparary reg
TEMP RPM
              res
TEMP LOCATION res
                   4
                         ;Temparary reg
              res 1
TEMP
                         ;Temparary variable
TEMP1
             res 1
;-----
#define DEBOUNCE H'02' ;Second bit in the FLAG_BYTE
#define TMR0_VALUE_L H'05E' ;Timer0 Higher byte value
#define TMR0_VALUE_H H'0AA' ;Timer0 Lower byte value
#define STEPS_PER_ROTATION H'30' ;Full steps per rotation = 360/step angle
;************************
STARTUP
        code 0x00
        goto Start
                          ;Reset Vector address
         CODE
               0x08
               ISR_HIGH
                         ;Higher priority ISR at 0x0008
```

```
PRG LOW
         CODE
                0x018
         goto
               ISR LOW
                           ;Lower priority ISR at 0x0018
;********************
PROG1
        code
Start
;Used only with MPLAB2000 + PCM18XA0- For Table read/write
;This code is not required when the actual device is used
;******************
        movlw
              0xb0
        movwf
                0xf9c
;This routine configures the I/O ports.
; PORTB - Outputs
;PORTB<3> - CNT1 - Used for switching PWM1 logic to change the
         direction of current in winding1
; PORTB<2> - CNT2 - Used for switching PWM2 logic to change the
          direction of current in winding2
;PORTB<4> - EN1 - Used for Enabling the H-bridge conrolling winding1
;PORTB<5> - EN2 - Used for Enabling the H-bridge conrolling winding2
; PORTD - Inputs
; PORTD<5> - Enable switch connected
; PORTD<6> - Forward/Reverse Tact switch connected
;PORTD<7> - INCH Tact switch connected
IO_PORT_Init
              0x0
         movlw
                          ;Clear PORTB
         movwf
                PORTB
         movlw
                0x0
                          ;Clear LatchB
               LATB
         movwf
        movlw
              0x03
                          ;PORTB<2:5> output,rest input
        movwf TRISB
                          ; PORTB<6:7> reserved for ICD
         movlw
                          ;Clear PORTD
              0x0
        movwf
               PORTD
         movlw
                0x0
                          ;Clear LatchD
                LATD
         movwf
         movlw
                0x0E7
                          ;PORTD<7:6> and <2:0> input,rest output
         movwf
                TRISD
; This routine configures Analog to Digital (ADC) module to read speed
;Referance voltage from the Preset connected to ADC Ch.0
ADC_Init
                          ; ADC Clock=Fosc/32, ADCCh=0, ADON=ON
         movlw
                0x81
         movwf
                ADCON0
                          ;ADC result left justified,
         movlw
                0 \times 04
         movwf ADCON1
                          ;ADC 1Ch.,(AD0);No ref.
         movlw
              0x00
                          ;Clear PortA bits
         movwf PORTA
         movlw 0x0F
                          ;PORTA<0:3> input, rest output
               TRISA
         movwf
                          ;Clear PORTE
         movlw
                0x0
         movwf
                PORTE
         movlw
                          ;Clear ADC result higher byte
                0x0
         movwf
                ADRESH
                          ;At POR AD reult is unknown
                          ;Clear ADC result lower byte
         movlw
                0x0
                ADRESL
                          ;At POR AD reult is unknown
        movwf
```

```
; This routine configures CCP1 and CCP2 as PWM outputs
; PWM Frequency set to 20KHz (PR2 register)
; ************************
CCP1_CCP2_Init
         movlw
                 0x00
                              ;CCP1 & CCP2 are outputs
         movwf
                 TRISC, ACCESS
         movlw
                 0x00
                 TMR2, ACCESS ; clear Timer2
         movwf
                              ;PR2=PWM Period;0xF9 corresponds to 20KHz
         movlw
                 0xF9
                 PR2,ACCESS
                             ;PWM period = [(PR2)+1]*4*Tosc*Tmr2 prescale
         movwf
                                       = [0xF9+1]*4*20MHz*16
         wlvom
               0 \times 0.4
                              ;Timer2 is ON,prescale = 1:1
         movwf T2CON, ACCESS ;Load to Timer2 control register
         movlw
               0x00c
                              ;Set CCP1 to PWM mode
         movwf CCP1CON, ACCESS ;
         movlw 0x00c ;Set CCP2 to PWM mode
                 CCP2CON, ACCESS ;
         movwf
;This routine initializes USART parameters
INIT_USART
         movlw
                 0x81
                              ;Baudrate = 9600
                 SPBRG
         movwf
         movlw
                 0 \times 24
                              ;8-bit transmission; Enable Transmission;
         movwf
                 TXSTA
                              ; Asynchronous mode with High speed transmission
         movlw
                 0x90
                              ; Enable the serial port
         movwf
                 RCSTA
                              ; with 8-bit continuous reception
;This routine initializes the Interrupts required
;TMRO overflow interrupt is used to change the step sequence
INTERRUPT init
         movlw
                 0x020
                             ;Unmask Timer0 interrupt
                              ;All other interrupts masked
         movwf
                 TNTCON
         movlw
                 0x004
                             ;TMR0 overflow interrupt-High priority
                INTCON2
         movwf
                             ;Power ON reset status bit/Brownout reset status bit
         movlw 0x093
         movwf RCON
                             ; and Instruction flag bits are set
                             ; Priority level on Interrupots enabled
                 0 \times 040
                             ;ADC Interrupt enabled
         movlw
                PIE1
         movwf
                0x000
                             ;A/D converter interrupt-Low priority
         movlw
                 IPR1
         movwf
         bsf
                 PIE1,5
         bcf
                 IPR1,5
         bsf
                 TRISC,7
; **********************
;Setting of jump count and prescale value based on the DIP switch settings
         clrf
              FLAG BYTE
                                    ;Intialising all local variables
         clrf
                 TEMP
                 SET_DIP_PARAMETERS
         call
                                    ; Parameters are set based on DIP switches
         call
                 STEPPER COM
                                    ;Displays a welcome message on the host PC screen
                 send command request
         call
                  ***********
```

```
;TimreO Initialization with prescaler
movlw
           TMR0 VALUE H
                            ;Timer0 Initialisation
            TMROH
    movwf
           SPEED REF H
    movwf
    movlw
           TMR0 VALUE L
    movwf
         TMR0L
    movwf
           SPEED REF L
;On POR, Motor is moved to a Full step positon
STEP_NUMBER ;starting from step0
           MOTOR DIRECTION, 0 ; motor in fwd direction
    bsf
    movlw
           0x0FF
                            ;Set CCPR1L 100% duty cycle
    movwf
           CCPR1L
                             ;8MSB's of duty cycle
    movlw
           0x30
                             ;2 LSB's at CCPxCON<5:4>
           CCP1CON, 1
    iorwf
    movlw
            0x000
                             ;set CCPR2L
            CCPR2L
    movwf
                             ;8MSB's of duty cycle
    movlw
            0x38
                             ;set Forward current in Winding1
    movwf
            PORTB
    bsf
           INTCON, PEIE
                            ; Enable all Unmasked peripheral interrupts
                            ; Enable all Unmasked interrupts
           INTCON, GIE
;Main program starts here which does the following
    1) Checks for Key pressed (with debounce)
         a) Motor Forward/Reverse Key connected to RD6
         b) Motor Inch (move by a step) Key connected to RD7
     2) If the step is updated by TimerO interrupt, outputs the
        required PWM on to CCP1/CCP2
    3) 1 and 2 are repeated continuously
MAIN LOOP
    btfsc
           PORTD, 5
                              ; Checking for DIP4 (Control enable) closed
           STOP MOTOR
                              ; If open, motor will not rotate
    goto
    call
            check_key
                              ; Routine which checks for FWD/REV and INCH keys
    btfsc
            FLAG BYTE, 4
                              ; If host PC gives command, process the command
            PROCESS COMMAND
    call
    btfss
           PIE1,ADIE
    goto
           MAIN_LOOP
           SET ADC GO
    call
    call
            SET DIP PARAMETERS
            MAIN LOOP
                              ; If not returning from TMR0 overflow interrupt
    goto
                              ;don't change the step, loop in Main routine
STOP MOTOR
           CCPR1L
    clrf
    clrf
           CCPR2L
    bcf
           CCP1CON, 4
    bcf
            CCP1CON, 5
                              ; Update the PWM duty cycle from the table
            CCP2CON, 4
    bcf
            CCP2CON, 5
    bcf
                              ;Update the PWM duty cycle from the table
    clrf
            STEP NUMBER
            MAIN LOOP
;On TMRO overflow program will execute the higher priority ISR
; Higher priority Interrupt Service Routine will update the Step count based
; on the Speed commanded by the Potentiometer read through ADC ch.0 in
;Low priority Interrupt
```

```
ISR HIGH
    btfsc
            INTCON, TMR0IF
                               ;Timer0 overflow Interrupt?
    goto
            timer0_int
                               ;Yes
    RETFIE
timer0 int
                               ;TMR0 overflow ISR
    call
            UPDATE STEP NUMBER
                               ;Upate the u-Step number
    call
            UPDATE PWM STEP
    movff
            SPEED REF H, TMROH
                               ;Load the Higher byte of SpeedCommand to TMROH
    movff
            SPEED_REF_L,TMR0L
                                ;Load the Lower byte of SpeedCommand to TMR0L
    btfsc
            FLAG_BYTE,6
            DECREMENT_INCH_COUNT
    call
    bcf
            INTCON, TMR0IF
                               ;Clear TMR0IF
    bcf
            FLAG_BYTE,0
                               ;Clear the flag for PWM updation
            RETFIE
;On ADC ch.0 interrupt program will execute the lower priority ISR
;Lower priority Interrupt Service Routine will read the ADC ch.0 result
; and load to the Speed command variables.
ISR LOW
    btfsc PIR1,ADIF
                              ;ADC Interrupt?
          ADC_SPEED_READ
    goto
                              ;Yes
    btfsc PIR1,RCIF
                               ;Recieve Interrupt?
    goto
          RECIEVE THE BYTE
                               ;Yes
    RETFIE
ADC SPEED READ
    movff
           ADRESH, RPM VALUE+1
                               ;Load AD result
    bcf
          STATUS, C
          RPM_VALUE+1,F
    rrcf
    movf
          RPM_VALUE+1,W
    btfsc
            STATUS, Z
    incf
            RPM VALUE+1,F
    bcf
            PIR1,ADIF
                               ;ADIF flag is cleared for next interrupt
    RETFIE
RECIEVE THE BYTE
    movff RCREG, RECIEVED BYTE
    movf
           RECIEVED BYTE, W
           load_RX_REG_from_WREG
    call
    bsf
           FLAG BYTE, 4
    bcf
            PIR1, RCIF
                               ;RCIF flag is cleared for next interrupt
    RETFIE
;This routine will update the PWM duty cycle on CCPx according to the count
; in STEP NUMBER. STEP NUMBER is updated in the TimerO overflow interrupt
UPDATE PWM STEP
;------
   movf STEP_JUMP,W ;Checking for full step
btfsc WREG,5 ;Yes, goto FULL_STEP_JUMP
goto FULL_STEP_JUMP ;No,then Half step/Microst
                             ;No,then Half step/Microstep
,------
;Below is the routine where for microstep(including halfstep) current(PWM) values
;from the sine table are taken and loaded to the CCPRxL and CCPxCON<5:4> as per Table-2
;Refer Table-2 Section 2.1 (microstep range from 0 to Half of a complete step)
```

```
movlw
              0x010
    cpfslt
             STEP NUMBER
                                   ; Is the u-step>0x10?
             step_half
    goto
                                  ;Yes, goto Step_half
    movlw
              0x00
    cpfseq
             STEP NUMBER
    goto
             cont_1_15
             UPPER sine_table
                                 ; Initialize Table pointer to the first
    movlw
    movwf
              TBLPTRU
                                  ;location of the table
    movlw
             HIGH sine_table
    movwf
              TBLPTRH
    movlw
             LOW sine table
    movwf
              TBLPTRL
    TBLRD*+
    TBLRD*+
              table_adjust_positive ;Used for skipping the table contents for
    call
cont 1 15
                                  ;u-steps < 32
                                  ;Is Motor Forward?
              MOTOR DIRECTION, 0
    btfsc
              fwd_1_15
    goto
    movlw
              0x38
                                   ;No Reverse, Wng1 current +ve, Wng2 current -ve
    goto
              rev_1_15
                                   ;Wng1-PORTB<3>;Wng2-PORTB<2>
fwd_1_15
    movlw
              0x3C
                                   ;Yes, Forward, Wng1 current +ve, Wng2 current +ve
rev_1_15
    movwf
              PORTB
             CCP2_INCREASE
                                   ;Load the PWM2 values and increment Table pointer
    call
    return
;Refer Table-2 Section 2.2 (microstep range from Half of a complete step to one complete step)
step half
    movlw
             0x020
                                  ; Is the u-step>20?
    cpfslt STEP NUMBER
                                   ;Yes, goto step_full
             step 1full
    goto
    movlw
              0x10
                                   ; Is the microstep == 10?
             STEP NUMBER
                                   ; No, continue loading PWM values
    cpfseq
    goto
              cont 16 31
    call
              point_to_end_of_table ;Yes,Point the Table pointer to end of the Table
cont_16_31
    btfsc
              MOTOR DIRECTION, 0
             fwd_16_31
    goto
    movlw
             0x38
                                   ;For Reverse rotation Wng1 current +ve
    goto
             rev_16_31
                                   ;Wng2 -ve
fwd_16_31
    movlw
              0x3C
                                   ;For forward rotation Wng1 current +ve, Wng2 +ve
rev_16 31
              PORTB
    movwf
    call
              CCP1 DECREASE
                                   ;Load the PWM1 values and decrement Table pointer
    return
;Refer Table-2 Section 2.3 (microstep range from One complete step to one and half step)
step_1full
    movlw
              0 \times 030
                                    ; Is u-step>30h?
    cpfslt
             STEP_NUMBER
                                    ;Yes, goto step_1nhalf
             step_1nhalf
    goto
    movlw
              0x20
                                    ;Is the microstep == 20?
             STEP NUMBER
    cpfseq
             cont 32 47
                                    ; No, continue loading PWM values
    goto
              point_to_begining_of_table
    call
cont_32_47
                                    ;Yes,Point the Table pointer to beginning of the Table
```

```
btfsc
              MOTOR DIRECTION, 0
              fwd 32 47
    goto
    movlw
              0x30
                                   ; If Motor is reverse Wng1&Wng2 current -ve
              rev_32_47
    goto
fwd_32_47
    movlw
              0x34
                                   ; If Motor is forward Wng1 current -ve, Wng2 +ve
rev 32 47
    movwf
              PORTB
              CCP1_INCREASE
    call
    return
;Refer Table-2 Section 2.4 (microstep range from One and half step to two complete step)
step_1nhalf
              0x40
    movlw
    cpfslt STEP NUMBER
             step_two
    goto
    movlw
              0 \times 30
                                   ; Is the microstep == 30?
              STEP NUMBER
    cpfseq
                                   ; No, continue loading PWM values
    goto
              cont_48_63
    call
              point_to_end_of_table ;Yes,Point the Table pointer to end of the Table
cont_48_63
    btfsc
              MOTOR DIRECTION, 0
    goto
              fwd 48 63
              0x30
                                   ; If Motor is reverse Wng1&Wng2 current -ve
    movlw
    goto
              rev_48_63
fwd_48_63
    movlw
              0x34
                                   ;If Motor is forward Wng1 current -ve,Wng2 +ve
rev_48 63
    movwf
              PORTB
    call
              CCP2_DECREASE
                                   ;Load the PWM2 values and decrement Table pointer
    return
;Refer Table-2 Section 2.5 (microstep range from Two complete step to two and half step)
step two
    movlw
              0x50
             STEP NUMBER
    cpfslt
    goto
              step 2nhalf
    movlw
              0x40
                                   ;Is the microstep == 40?
              STEP NUMBER
    cpfseq
                                   ; No, continue loading PWM values
              cont 64 79
    goto
    call
              point to begining of table
cont_64_79
                                   ;Yes,Point the Table pointer to begining of the Table
              MOTOR_DIRECTION, 0
    btfsc
    goto
              fwd_64_79
    movlw
              0x34
                                   ; If Motor is reverse Wnql current -ve, Wnq2 +ve
    goto
              rev_64_79
fwd 64 79
                                    ; If Motor is forward Wng1&Wng2 current -ve
    movlw
              0x30
rev_64_79
    movwf
              PORTB
    call
              CCP2 INCREASE
                                   ;Load the PWM2 values and increment Table pointer
;------
;Refer Table-2 Section 2.6 (microstep range from two and half step to Three complete step)
step 2nhalf
    movlw
              0x60
    cpfslt
              STEP NUMBER
              step_three
    goto
```

```
movlw
             0x50
                                     ; Is the microstep == 50?
             STEP NUMBER
    cpfseq
             cont 80 95
                                    ; No, continue loading PWM values
    goto
             point_to_end_of_table
                                     ;Yes,Point the Table pointer to end of the Table
    call
cont_80_95
    btfsc
             MOTOR DIRECTION, 0
    aoto
             fwd 80 95
    movlw
             0x34
                                     ;If Motor is reverse Wng1 current is -ve,Wng2 +ve
             rev_80_95
    goto
fwd_80_95
    movlw
             0x30
                                     ; If Motor is forward Wng1&Wng2 current -ve
rev_80_95
    movwf
             PORTR
             CCP1_DECREASE
    call
                                     ;Load the PWM1 values and decrement Table pointer
    return
;-----
;Refer Table-2 Section 2.7 (microstep range from 3 complete step to 3 and half step)
step_three
    movlw
             0x70
             STEP_NUMBER
    cpfslt
             step_3nhalf
    goto
    movlw
             0x60
                                     ; Is the microstep == 60?
    cpfseq STEP NUMBER
                                     ; No, continue loading PWM values
    goto
             cont 96 111
    call
            point to begining of table
cont_96_111
                                     ;Yes,Point the Table pointer to begining of the Table
             MOTOR DIRECTION, 0
    btfsc
             fwd_96_111
    goto
    movlw
             0x3C
                                     ; If Motor is reverse Wng1&Wng2 current +ve
    goto
             rev_96_111
fwd 96 111
                                     ; If Motor is Forward Wng1 current +ve, Wng2 -ve
    movlw
             0x38
rev 96 111
             PORTB
    movwf
    call
             CCP1 INCREASE
                                     ;Load the PWM1 values and increment Table pointer
    return
;Refer Table-2 Section 2.8 (microstep range from 3 and Half step to 4 complete step/0 step)
,-----
step_3nhalf
    movlw
             0x80
             STEP NUMBER
    cpfslt
    goto
             CLEAR STEP NUMBER
             0x70
                                    ; Is the microstep == 70?
    movlw
             STEP NUMBER
    cpfseq
    goto
             cont 112 127
                                    ; No, continue loading PWM values
                                    ; Yes, Point the Table pointer to end of the Table
    call
             point_to_end_of_table
cont 112 127
             MOTOR DIRECTION, 0
    btfsc
             fwd 112_127
    goto
             0x3C
                                     ; If Motor is reverse Wnq1&Wnq2 current +ve
    movlw
    goto
             rev 112 127
fwd_112_127
                                     ; If Motor is forward Wng1 current +ve, Wng2 -ve
    movlw
             0x38
rev_112_127
             PORTB
    movwf
             CCP2 DECREASE
                                     ;Load the PWM2 values and decrement Table pointer
    call
    return
;If Full step control is choosen, both PWM's will be loaded with 100%
;duty cycle during initialisation and current sequence for the steps is
```

```
; controlled by Enable signals EN1(RB4) and EN2(RB5) and PWM switching signals
;CNT1(RB3) and CNT2(RB2).
;Refer Table-1
FULL_STEP_JUMP
    movlw
             0x20
                                    ; check for 1st step
    cpfslt
             STEP NUMBER
    goto
           SECOND STEP
            MOTOR_DIRECTION, 0
    btfsc
                                    ;Test motor_direction
    goto
             FWD FIRST STEP
    movlw
             0x28
                                     ; If Motor is reverse Wng1=0, Wng2=-1
    movwf
             PORTB
    call
             CCP1_LOW_CCP2_HIGH
    return
FWD FIRST STEP
    movlw
             0x18
                                     ;If Motor is forward Wng1=+1,Wng2=0
    movwf
              PORTB
              CCP1_HIGH_CCP2_LOW
    call
    return
SECOND STEP
    movlw
              0x40
                                     ; check for 2nd step
    cpfslt
              STEP NUMBER
              THIRD_STEP
    goto
    btfsc
             MOTOR DIRECTION, 0
                                     ;Test motor_direction
    goto
             FWD SECOND STEP
             0x14
                                     ; If Motor is reverse Wng1=-1, Wng2=0
    movlw
    movwf
             PORTB
    call
             CCP1_HIGH_CCP2_LOW
    return
FWD SECOND STEP
    movlw
             0x24
                                     ;If Motor is forward Wng1=0, Wng2=+1
    movwf
             PORTB
             CCP1 LOW CCP2 HIGH
    call
    return
THIRD STEP
    movlw
             0x60
                                     ;check for 3rd step
    cpfslt
             STEP NUMBER
             FORTH STEP
    goto
             MOTOR DIRECTION, 0
    btfsc
                                     ;Test motor direction
    goto
             FWD THIRD STEP
    movlw
              0x24
                                     ;If Motor is reverse Wng1=0,Wng2=+1
    movwf
              PORTB
    call
              CCP1 LOW CCP2 HIGH
    return
FWD THIRD STEP
    movlw
             0x14
                                     ;If Motor is forward Wng1=-1, Wng2=0
    movwf
             PORTB
    call
             CCP1_HIGH_CCP2_LOW
    return
FORTH STEP
    movlw
              0x80
                                     ;check for 4th step
             STEP NUMBER
    cpfslt
             CLEAR_STEP_NUMBER
    goto
    btfsc
             MOTOR DIRECTION, 0
                                     ;Test motor direction
    goto
             FWD FORTH STEP
    movlw
             0x18
                                     ;If Motor is reverse Wng1=+1,Wng2=0
             PORTB
    movwf
             CCP1_HIGH_CCP2_LOW
    call
    return
FWD FORTH STEP
    movlw
              0x28
                                     ;If Motor is forward Wng1=0,Wng2=-ve
    movwf
              PORTB
              CCP1 LOW CCP2 HIGH
    call
    return
CLEAR_STEP_NUMBER
```

clrf

STEP NUMBER

```
return
;This routine checks for the keys pressed after waiting for the key to debounce
        a) Motor Forward/Reverse Key connected to RD6
           Toggle switch, toggles between Forward and reverse with each press
        b) Motor Inch Key connected to RD
           Moves the motor by a step with each press in the direction selected
           by Fwd/Rev key previously
check key
   btfsc
          PORTD, 7
                                  ; Is key pressed "INCH"?
          check fwd rev key
   goto
          key debounce
                                  ;Yes, wait for debounce
   btfss
          FLAG_BYTE, DEBOUNCE
   return
         FLAG_BYTE, DEBOUNCE
   bcf
                                  ; If key pressed < debounce time,
           INTCON, TMR0IE
                                  ; If debounced, Disable TimerO interrupt
   bcf
            PIE1, ADIE
    call
           UPDATE STEP NUMBER
                                  ;Update the step
           SET_DIP_PARAMETERS
    call
          UPDATE PWM STEP
   call
   bcf
           FLAG BYTE, 0
                                  ;Clear the flag for PWM updation
    return
check_fwd_rev_key
          PORTD, 6
   btfss
                                   ;Fwd/Rev key pressed?
           fwd_rev_key_pressed
   goto
   clrf
           COUNTER
                                   ; No, clear debounce counter
   clrf
           COUNTER1
                                   ; No, clear debounce counter
   return
fwd_rev_key_pressed
   call
           key debounce
                                  ;Yes Fwd/Rev key pressed, wait to debounce
   btfss
           FLAG BYTE, DEBOUNCE
   return
         FLAG_BYTE, DEBOUNCE
   bcf
                                 ; If key pressed < debounce time,
           INTCON, TMR0IE
   bsf
                                  ;Enable Timer0 Interrupt
           PIE1,ADIE
   btfsc
           MOTOR_DIRECTION, 0
    goto
           set revdirction bit
   bsf
           MOTOR DIRECTION, 0
                                  ;Set Motor direction bit to Forward
   return
set revdirction bit
   bcf
           MOTOR DIRECTION, 0
                                  ;Set Motor direction bit to Reverse
   return
; This routine Updates the step count depending upon the Number of Microsteps/step
;entered by the user
UPDATE STEP NUMBER
          STEP JUMP, W
   movf
   addwf
          STEP NUMBER, 1
                                  ; Add step jump count to the present step number
   btfsc STEP NUMBER,7
                                  ; If Step number = 80h then clear the count
   clrf
           STEP NUMBER
   return
;This routine waits for key to debounce after it is pressed the count value is 0x3ff
key_debounce
   incf
           COUNTER, 1, ACCESS
                                  ;After key press is senced, increment COUNTER
   movlw
                                  ; If counter == 0xFF
          0x12
   cpfseq COUNTER, ACCESS
           return_from_debounce
```

```
incf
             COUNTER1,1,ACCESS
                                        ; Increment Counter1
    movlw
                                        ; If counter1 == 0x3
    cpfseq
             COUNTER1, ACCESS
             return_from_debounce
    goto
              FLAG_BYTE, DEBOUNCE, ACCESS ;Set debounce flag(key press success)
    bsf
    return
return from debounce
    bcf
             FLAG BYTE, DEBOUNCE, ACCESS
                                        ; If key pressed < debounce time,
    return
                                        ; Key press is not sucessful
;This routine sets the number of microsteps based on the DIP switch settings.
; Also this sets the TMRO prescale value based on the number of microsteps.
SET DIP PARAMETERS
    movlw
                                        ;DIP switches connected to PORTD<2:0>
    andwf
             PORTD, W
                                        ;Other bits removed
             TEMP
    movwf
    bsf
             STATUS, C
    movlw
              0x7
    subfwb
             TEMP, F
    movff
             TEMP, MICRO STEPS
SET_MICROSTEPS
             0 \times 01
                                        ; Is microsteps/step setting==1?
    movlw
    cpfseq
             TEMP
    goto
             CHECK FOR 2
                                        ; No, check for next
    movlw
             0x20
                                        ;Yes, then
    movwf
             STEP JUMP
                                        ;STEP_JUMP = 20h
                                        ;Load the TOCON with value
             0X86
    movlw
    movwf
             TOCON
                                        ;TMR0 ON and prescalar is 1:64
    return
CHECK FOR 2
             0x02
                                        ; Is microsteps/step setting==2?
    movlw
    cpfseq
             TEMP
             CHECK FOR 3
                                        ; No, check for next
    goto
    movlw
             0x10
                                        ;Yes, then
    movwf
             STEP_JUMP
                                        ;STEP JUMP = 10h
                                        ;Load the TOCON with value
             0X85
    movlw
    movwf
             TOCON
                                        ;TMR0 ON and prescalar is 1:32
    return
CHECK_FOR_3
    movlw
              0x03
                                        ; Is microsteps/step setting==4?
    cpfseq
             TEMP
    goto
             CHECK FOR 4
                                        ; No, check for next
    movlw
             0x08
                                        ;Yes, then
    movwf
             STEP JUMP
                                        ;STEP JUMP = 08h
                                        ;Load the TOCON with value
    movlw
             0X84
    movwf
             TOCON
                                        ;TMR0 ON and prescalar is 1:16
    bsf
              FLAG BYTE, 3
                                        ;Set FLAG BYTE<3> for table manipulation
    return
CHECK FOR 4
              0x04
                                        ; Is microsteps/step setting==8?
    movlw
             TEMP
    cpfseq
             CHECK FOR 5
                                        ; No, check for next
    goto
    movlw
             0x04
                                        ;Yes, then
    movwf
             STEP JUMP
                                        ; STEP JUMP = 4h
             0X83
                                        ;Load the TOCON with value
    movlw
             TOCON
                                        ;TMR0 ON and prescalar is 1:8
    movwf
    bsf
              FLAG BYTE, 3
                                        ;Set FLAG_BYTE<3> for table manipulation
    return
CHECK_FOR_5
    movlw
              0x05
                                        ; Is microsteps/step setting==16?
    cpfseq
              TEMP
              CHECK FOR 6
                                        ; No, check for next
    goto
    movlw
              0x02
                                        ;Yes, then
```

```
movwf
           STEP JUMP
                                    ;STEP JUMP = 2h
            0X82
                                    ;Load the TOCON with value
    movlw
   movwf
            TOCON
                                    ;TMR0 ON and prescalar is 1:4
   bsf
           FLAG_BYTE, 3
                                    ;Set FLAG_BYTE<3> for table manipulation
   return
CHECK FOR 6
   movlw
            0x01
                                    ;Yes, then
   movwf
           STEP JUMP
                                    ;STEP JUMP = 1h
           0X81
                                    ;Load the TOCON with value
   movlw
   movwf
           TOCON
                                    ;TMR0 ON and prescalar is 1:2
   return
;This routine reads the PWM values from the table and loads to the CCPR1L and
;CCP1CON<5:4> and increments Table pointer to next value appropriately,
; based on the number of microsteps selected
CCP1 INCREASE
   TBLRD*+
   movff
           TABLAT, CCPR1L
   TBLRD*+
           CCP1CON, 4
                                    ;Update the PWM duty cycle from the table
   bcf
   bcf
           CCP1CON, 5
           TABLAT, 0
   movf
   iorwf
           CCP1CON, 1
   btfsc
          FLAG BYTE, 3
    call
           table adjust positive
                                   ;Update the table for next value
   return
;This routine reads the PWM values from the table and loads to the CCPR1L and
;CCP1CON<5:4> and decrements Table pointer to next value appropriately,
; based on the number of microsteps selected
CCP1 DECREASE
   TBLRD*-
   bcf
          CCP1CON, 4
   bcf
           CCP1CON,5
                                    ;Update the PWM duty cycle from the table
   movf
           TABLAT, 0
           CCP1CON, 1
    iorwf
    TBLRD*-
    movff
           TABLAT, CCPR1L
   btfsc
           FLAG BYTE, 3
   call
           table_adjust_negative
                                  ;Update the table for next value
   return
;This routine reads the PWM values from the table and loads to the CCPR2L and
;CCP2CON<5:4> and increments Table pointer to next value appropriately,
; based on the number of microsteps selected
CCP2 INCREASE
   TBI-RD*+
   movff
           TABLAT, CCPR2L
                                   ; Read the values from Table and update PWM duty cycle
   TBLRD*+
                                   ;(10 bits)of CCP2
   bcf
           CCP2CON, 4
   bcf
           CCP2CON, 5
           TABLAT, 0
   movf
           CCP2CON, 1
   iorwf
   btfsc
           FLAG BYTE, 3
           table adjust positive
                                   ;Adjust the table pointer
    call
    return
;This routine reads the PWM values from the table and loads to the CCPR2L and
;CCP2CON<5:4> and decrements Table pointer to next value appropriately,
```

```
; based on the number of microsteps selected
CCP2 DECREASE
   TBI-RD*-
          CCP2CON, 4
   bcf
   bcf
         CCP2CON, 5
   movf
         TABLAT, 0
   iorwf CCP2CON, 1
   TBLRD*-
   movff
          TABLAT, CCPR2L
   btfsc
          FLAG BYTE, 3
   call
          table_adjust_negative
                             ; Update the table for next value
   return
; This routine adjusts the Table pointer to the begining of the Table,
; which is changed due to the previous table operations
point_to_begining_of_table
   TBLRD*+
   TBLRD*+
   TBLRD*+
   call
          table_adjust_positive
                             ;Used for skipping the table contents for
                 ;ustep<16
   btfsc
          FLAG BYTE, 3
   call
          table adjust positive
                             ; Update the table for next value
   return
; This routine adjusts the Table pointer to the end of the Table,
; which is changed due to the previous table operations
point to end of table
   TBLRD*-
   TBLRD*-
   TBLRD*-
   call
          table adjust negative
                           ;Used for skipping the table contents for
                            ;ustep<16
          FLAG BYTE, 3
   btfsc
   call
          table adjust negative
                           ;Upadte the table for next value
   return
; This routine advances the Table pointer by (STEP JUMP-1) *2 times,
; used for Table pointer updation for -16,-8,-4,-2 microsteps/step
table_adjust_positive
   movf STEP_JUMP,W
   dcfsnz
         WREG, F
                             ;W= STEP JUMP-1
   return
   rlncf
          WREG,W
                             ;W=(STEP JUMP-1)*2
         STATUS, C
   baf
   addwfc TBLPTRL, F
                             ;TablePointer= Table_pointer+W
   clrf
          WREG
   addwfc TBLPTRH, F
   addwfc TBLPTRU, F
   return
; This routine updates (subtracts) the Table pointer by (STEP JUMP-1) *2 times,
;used for Table pointer updation for -16,-8,-4,-2 microsteps/step
table_adjust_negative
   movf
         STEP JUMP, W
   dcfsnz WREG, F
                             ; W= STEP_JUMP-1
```

```
return
         WREG, W
                             ; W=(STEP JUMP-1)*2
   rlncf
   bsf
         STATUS, C
   subwfb TBLPTRL, F
                             ;TablePointer= Table_pointer-W
         WREG
   clrf
   subwfb TBLPTRH, F
   subwfb TBLPTRU, F
   return
;Make PWM1 high and PWM2 Low
CCP1 HIGH CCP2 LOW
                            ;Set CCPR1L 100% duty cycle
   movlw
       0 \times 0 FF
        CCPR1L
                            ;8MSB's of duty cycle
   movwf
   movlw
        0x30
                             ;2 LSB's at CCPxCON<5:4>
   iorwf
        CCP1CON, F
                             ;Set CCP2 duty cycle to 0%
   movlw
         0X0
   movwf
         CCPR2L
   movlw
         0X0CF
   andwf
         CCP2CON, F
   return
; Make PWM1 Low and PWM2 High
CCP1_LOW_CCP2_HIGH
       0x0
   movlw
                             ;Set CCPR1L 0% duty cycle
         CCPR1L
   movwf
                             ;8MSB's of duty cycle
   movlw
         0x0CF
                             ;2 LSB's at CCPxCON<5:4>
   andwf
         CCP1CON, F
        0X0FF
   movlw
                             ;Set CCP2 duty cycle to 100%
         CCPR2L
   movwf
   movlw
        0X030
   iorwf
        CCP2CON, F
   return
; This routine sets the ADC GO bit high after an aquisition time of 20uS approx.
SET_ADC_GO
   call
         CALCULATE RPM1
   btfss
        ADCON0,GO
   bsf
         ADCON0, GO
                             ;Set GO bit for ADC conversion start
   return
;This routine calculates the RPM with Potentiometer
CALCULATE RPM1
   clrf
       TEMP
         TEMP1
   clrf
        RPM_VALUE+1,W
   movf
   btfsc
        STATUS, Z
   incf
        RPM VALUE+1,F
   movlw
        0x2F
         TEMP_LOCATION
   movwf
   movlw
         0xAE
   movwf
         TEMP LOCATION+1
continue subtraction1
   bsf
         STATUS, C
   movf
         RPM VALUE+1,W
         TEMP_LOCATION+1,F
   subwfb
   movlw
         0x0
   subwfb
        TEMP_LOCATION, F
```

```
btfss
                                  STATUS, C
            goto
                                  keep result in rpm1
            incf
                                  TEMP, F
                                 STATUS, C
            btfsc
                               TEMP1.F
            incf
            goto
                                 continue subtraction1
keep_result_in_rpm1
 ;Timer0 value = FFFF-Timer0
            rlcf
                                  TEMP, F
            rlcf
                                  TEMP1,F
           bcf
                                 STATUS, C
                                TEMP,F
           rlcf
                              TEMP1,F
           rlcf
           bsf
                               STATUS, C
            movlw
                                 0xFF
            subfwb
                               TEMP, F
           subfwb
                                 TEMP1,F
            movff
                                  TEMP1, SPEED REF H
            movff
                                  TEMP, SPEED REF L
            return
 ; Table for the microsteps.
; Even numbered values are loaded to CCPRxL and
;Odd numbered values are ORed with CCPxCON(CCPxCON<5:4>)
;to load complete 10 bits of PWMx duty cycle
TABLE
                       code 0x02A0
sine table db
                                    0x0,0x00,0x20,0x30,0x34,0x20,0x42,0x10,0x50,0x0,0x5C,0x10,0x68,0x20
                         db
                                          0 \times 74, 0 \times 30, 0 \times 80, 0 \times 30, 0 \times 8C, 0 \times 30, 0 \times 98, 0 \times 30, 0 \times A4, 0 \times 20, 0 \times B0, 0 \times 10, 0 \times C3, 0 \times 10, 0 \times C3, 0 \times 10, 0 \times C3, 0 \times C3, 0 \times C3, 0 \times C4, 0 \times 
                          db
                                        0xD6,0x20,0xEA,0x20,0xFF,0x30
                        *******************
; This routine loads the data in Wreg to Transmission register (TXREG) after checking
; of completion of previously loaded byte transmission
 load_RX_REG_from_WREG
                                  PIR1,TXIF
                                   load_RX_REG_from_WREG
            goto
            movwf
                                   TXREG
            return
 ;This routine processes the command from host PC. If is is command, then
 ;FLAG_BYTE<5> is set and data is awaited.
PROCESS COMMAND
           bcf
                               FLAG BYTE, 4
                                                                                                       ;Flag for byte recieved interrupt
           btfsc
                              FLAG_BYTE,5
                                                                                                        ;Flag for differanciating Command and data
                                                                                                        ; If set, recieved_byte is data
            goto
                                  CHECK DATA
            movff
                                  RECIEVED BYTE, COMMAND BYTE
           bcf
                                  PIE1,ADIE
                                                                                                        ; Enable Speed ref from Pot
                                  INTCON, TMR0IE
           bcf
                                                                                                       ;Enable TimerO interrupt
CHECK DATA
                                   0x030
                                                                                                        ; Is Received command=exit from steup?
            movlw
            cpfseq
                                  COMMAND BYTE
                                  CHECK_FOR_STEP_VALUE
            goto
                                                                                                       ; No, check for change of usteps?
                                  DISPLAY EXIT SETUP
                                                                                                       ;Display exit from setup
            call
            call
                                  SET DIP PARAMETERS
                                                                                                       ;set the microsteps according to the DIP switches
            bcf
                                  FLAG_BYTE,5
```

```
bcf
              FLAG BYTE, 6
    bcf
              FLAG BYTE, 0
    bsf
              PIE1,ADIE
                                          ;Enable Speed ref from Pot
    bsf
             INTCON, TMR0IE
                                          ;Enable Timer0 interrupt
    return
CHECK FOR STEP VALUE
    movlw 0x031
                                          ; Is Received command=change no. of usteps?
            COMMAND_BYTE
    cpfseq
    goto
             CHECK FOR DIRECTION
                                          ; No, check for direction change
    btfsc
             FLAG BYTE, 5
             SET_MICROSTEPS_DATA
    goto
    call
             DISPLAY_STEPS_VALUE
                                          ;Display allowed microstep values
    bsf
             FLAG_BYTE,5
    return
SET_MICROSTEPS_DATA
    movlw 0x30
                                          ;Data received for microstep
            RECIEVED BYTE, W
    subwf
           MICRO_STEPS
    movwf
             STATUS, Z
    btfsc
    goto
             NOT_VALID_ENTRY
    movlw
             0x7
    cpfslt MICRO_STEPS
    goto
            NOT VALID ENTRY
           MICRO STEPS, TEMP
    movff
    call
            SET MICROSTEPS
                                          ;Set the microsteps according to the data entry
    movlw
             0xA
    call
             load_RX_REG_from_WREG
    bcf
             FLAG BYTE,5
    return
;-----
CHECK FOR DIRECTION
    movlw 0x032
                                          ; Is Received command=change of direction?
    cpfseq COMMAND BYTE
             CHECK FOR INCH STEP
                                          ; No, check for No. of steps to inch
    goto
    btfsc
            FLAG BYTE,5
            SET_DIRECTION_DATA
    goto
             DISPLAY STEPS DIRECTION
                                          ;Display allowed direction values
    call
    bsf
             FLAG BYTE,5
    return
SET DIRECTION DATA
                                          ;Test the recieved data for Forward
    movlw
             0x30
    cpfseq RECIEVED BYTE
            test_0x31
    goto
            MOTOR_DIRECTION
    clrf
                                          ;Direction commanded is forward
            MOTOR_RUN_FORWARD
    call
    bsf
             INTCON, TMR0IE
                                          ;Enable Timer0 interrupt
    bcf
              FLAG BYTE, 5
    return
test_0x31
             0x31
    movlw
                                          ; Check for reverse rotation command
    cpfseq RECIEVED BYTE
    goto
             NOT VALID ENTRY
    movlw
             0x1
             MOTOR DIRECTION
                                          ; Yes, set the motor direction to reverse
    movwf
            MOTOR_RUN_REVERSE
    call
    bsf
              INTCON, TMR0IE
                                          ;Enable Timer0 interrupt
    bcf
              FLAG BYTE, 5
    return
CHECK FOR INCH STEP
              0x033
    movlw
                                          ; Is Received command=Inch step?
    cpfseq COMMAND_BYTE
```

```
goto
              CHECK FOR RPM
                                            ; No, check for RPM of motor
              FLAG BYTE, 5
    btfsc
              SET INCH STEPS DATA
    goto
              DISPLAY_STEPS_INCH
                                            ;Display allowed INCH step values
    call
              HIGH(RPM_VALUE+3)
    movlw
    movwf
              FSR2H
    movlw
              LOW(RPM VALUE+3)
    movwf
           FSR2L
                                            ;Allow 3 digits of hata to be entered
    movlw
              0xFF
    movwf
              POSTDEC2
    movwf
              POSTDEC2
    movwf
              POSTDEC2
    movwf
              TNDF2
    bsf
              FLAG_BYTE,5
    return
SET INCH STEPS DATA
    movlw
              0x2F
              RECIEVED_BYTE
    cpfsgt
    goto
              INCH COMMAND READ
                                            ;Check the data entered >0<9
    movlw
              0x3A
              RECIEVED_BYTE
                                            ;If the data is other than 0-9,
    cpfslt
    goto
              INCH COMMAND READ
                                            ; consider that data entry is over
    movlw
              0x30
    subwf
              RECIEVED BYTE, W
    movwf
              POSTINC2
                                            ;Store the data in the RPM VALUE
    return
INCH COMMAND READ
    call MERGE_NUMBERS
                                            ;Concatinate the entered numbers
             CONVERT_TO HEX
    call
                                            ;Convert the concatinated nubers to Hex
    movff
              RPM VALUE, INCH VALUE
                                            ;Load the hex value to the registers INCH VALUE
    movff
              RPM VALUE+1, INCH VALUE+1
    bsf
              INTCON, TMR0IE
                                            ;Enable Timer0 overflow interrupt
             FLAG BYTE, 6
    bsf
    bcf
             FLAG BYTE,5
    movlw
              0xA
    call
              load RX REG from WREG
    return
;-----
CHECK FOR RPM
    movlw
              0x034
                                            ; Is Received command=set RPM?
    cpfseq
              COMMAND BYTE
              NOT VALID ENTRY
    goto
                                            ; No, Not a correct command
    btfsc
              FLAG BYTE, 5
              SET RPM DATA
    goto
              DISPLAY STEPS RPM
                                            ;Display allowed RPM range
    call
    movlw
              HIGH(RPM_VALUE+3)
    movwf
            FSR2H
    movlw
              LOW(RPM_VALUE+3)
    movwf
              FSR2L
              0xFF
    movlw
    movwf
              POSTDEC2
              POSTDEC2
    movwf
    movwf
             POSTDEC2
    movwf
              INDF2
    bsf
              FLAG BYTE, 5
    return
SET RPM DATA
    movlw
              0x2F
                                            ;Check the data entry range 0 to 9
    cpfsgt
              RECIEVED BYTE
              RPM COMMAND READ
    goto
    movlw
              0x3A
    cpfslt
              RECIEVED BYTE
              RPM COMMAND READ
    goto
    movlw
              0x30
                                            ;Store th ereceived data in the RPM_VALUEs
    subwf
              RECIEVED_BYTE, W
```

```
movwf
           POSTINC2
   return
RPM COMMAND READ
          PIE1,ADIE
                               ;Disable Speed ref from Pot
   bcf
          convert_RPM_to_HEX
                               ;Convert the data to Hex
   call
          CALCULATE RPM
                               ;Calculate the TimerO value from the RPM data entered
   call
   call
          display motor direction
   bsf
          INTCON, TMR0IE
                               ;Enable Timer0 ineterrupt
          FLAG_BYTE,5
   bcf
   return
;-----
NOT VALID ENTRY
   call
          DATA NOT VALID
                              ; If the entered command is other than the valid commands,
   call
           send command request
                               ; then display the DATA_INVALID
   bcf
           FLAG BYTE, 5
   return
; This routine decrements the INCH count given from host PC to move in INCH mode
DECREMENT_INCH_COUNT
   decf
        INCH VALUE+1,F
                              ;Decrement the inching counter
   btfss
           STATUS, C
                              ;after each step movement
          INCH VALUE, F
   decf
   btfsc
          STATUS, C
   return
   bcf
          FLAG BYTE,5
                              ; If all steps are over, clear the flag bytes
   bcf
          FLAG BYTE, 6
          INTCON, TMR0IE
   bcf
                              ; If Inch_count == 0, Disable Timer 0 interrupt
   return
; RPM value from the Host PC is entered in descrete decimal digits.
;This routine merges the digits to get the RPM value and then
; converts the RPM in decimal value to Hex value
convert RPM to HEX
   clrf TEMP
          TEMP1
   clrf
   call
           MERGE NUMBERS
                              ; Merge the individual digits
           CONVERT_TO_HEX
                              ;Convert the entered number to Hex
   call
   movlw
           0xC8
                              ;RPM limit set to 200 RPM
          RPM VALUE+1
   cpfslt
   movwf
           RPM VALUE+1
; RPM value from the Host PC is entered in descrete decimal digits.
;This routine merges the numbers
MERGE NUMBERS
                             ;Third digit is checked for number
   movlw
           0xA
           RPM VALUE+2
   cpfslt
           check second
                             ; If it has a valid number, conacatinate
   goto
   swapf
          RPM VALUE+1,W
                             ; RPM VALUE2 and 3 and keep in RPM VALUE2
   iorwf
          RPM VALUE+2,W
   movwf
           RPM VALUE+1
   return
check second
   movlw
                              ; If second digit is a valid number,
           0xA
   cpfslt
           RPM VALUE+1
                              ; concatinate RPM_VALUE2 and 1 and place in
   goto
           place_number
                              ;RPM_VALUE2
           RPM VALUE, W
   swapf
   iorwf
           RPM VALUE+1,F
   clrf
           RPM_VALUE
```

```
return
place number
    movff
             RPM VALUE, RPM VALUE+1
                                     ; If only one digit is entered, keep it in
    clrf
             RPM_VALUE
                                     ;RPM_VALUE2 and clear RPM_VALUE1
    return
;-----
    movlw
           HIGH(RPM_VALUE+2)
    movwf
            FSR2H
    movlw
             LOW(RPM VALUE+2)
    movwf
             FSR2L
    movlw
             0xA
                                     ;Third digit is checked for number
    cpfslt
             POSTDEC2
    goto
             check second
                                    ; If it has a valid number, conacatinate
          POSTINC2, W
                                     ;RPM_VALUE2 and 3 and keep in RPM_VALUE2
    swapf
    iorwf
            POSTDEC2, W
    movwf
            INDF2
    return
;check second
    movlw
                                     ; If second digit is a valid number,
             0xA
    cpfslt
             POSTDEC2
                                     ;concatinate RPM_VALUE2 and 1 and place in
    goto
             place_number
                                     ;RPM_VALUE2
    swapf
             INDF2,F
            POSTINC2, W
    movf
    iorwf
             INDF2,F
    clrf
             RPM VALUE
    return
;place_number
             RPM_VALUE, RPM_VALUE+1
    movff
                                   ; If only one digit is entered, keep it in
                                    ;RPM VALUE2 and clear RPM_VALUE1
    clrf
             RPM VALUE
    return
;This routine converts the decimal RPM value to Hex
CONVERT TO HEX
    clrf
             TEMP
contimue conversion hex
        STATUS, C
    bsf
                                    ;Divide the number by 0x16 by
    movlw
                                    ;Subtracting continuously 0x16 from the
             0x16
    subwfb
             RPM VALUE+1,F
                                     ;value
    movlw
             0x0
                                     ;The result is stored in TEMP
    subwfb
             RPM VALUE, F
    btfss
           STATUS, C
            set LS nibble
    goto
    movlw
           0x9F
    cpfsgt RPM VALUE+1
    goto
           check_ls_nibble
    movlw
             0x60
    subwf
             RPM VALUE+1,F
check ls nibble
    movf
            RPM VALUE+1,W
    andlw
             0x0F
             TEMP1
    movwf
    movlw
             0x9
    cpfsgt
             TEMP1
    goto
             increment_count
    movlw
             0x6
    subwf
             RPM_VALUE+1,F
increment count
    incf
             TEMP,F
    goto
             contimue_conversion_hex
set LS nibble
    movlw
              0x16
```

```
addwf
            RPM VALUE+1, F
            0x9
    movlw
            RPM VALUE+1
    cpfsgt
            set_hex_value
    goto
    movlw
            0x6
    subwf
            RPM VALUE+1,F
set hex value
    swapf
           TEMP, W
                              ;Set the values in RPM_VALUE(H) and
            0xF0
                              ;RPM_VALUE+1(L)
    andlw
    iorwf
           RPM VALUE+1,F
            TEMP,W
    swapf
    andlw
            0x0F
   movwf
            RPM VALUE
   return
;TimerO reload value is calculated from the RPM VALUE and the microsteps setting
;Calculation of TimerO reload value:
                            (A) = (RPM/60) * microsteps
;Step1: No. of steps/sec
                            (B) = 1/A
;Step2: Time/1 step
;Step3: Timer0 counts for B sec (C) = B * 20 MHz
;Step4: Divide by Timer0 prescaler (D) = C/Timer0 prescaler
;Step5: Subtract count from FFFFh, E = FFFF - D
;So E is the TimerO reload value
CALCULATE RPM
   movf
          RPM VALUE+1,W
   btfsc
          STATUS, Z
          WREG,W
    incf
          STEPS_PER_ROTATION
    mullw
                              ;RPM*No of steps/revolution
    movff
           PRODL, TEMP RPM+2
    movff
           PRODH, TEMP RPM+1
           TEMP_RPM
    clrf
           TOCON, W
    movf
    andlw
           0x07
    addwf
           MICRO STEPS, W
    movwf
           TEMP
   bcf
           STATUS, C
repeat_shift_left
           TEMP RPM+2,F
                             ;(RPM*No of steps/revolution)*
   rlcf
    rlcf
            TEMP RPM+1,F
                              ; (TMR0 prescaler*No. of steps/step)
    rlcf
           TEMP RPM, F
    decfsz
           TEMP, F
           repeat shift left
    aoto
    call
           divide_3_8zero_by_rpm
   return
; (60/0.2*10-6)=300000000d = 11E1A300h
;Timer0 value for RPM = 11E1A300h/((steps/revolution*Timer0 prescaler value)*(RPM))
divide_3_8zero_by_rpm
   clrf
           TEMP
    clrf
           TEMP1
                              ;Temp_loation =0x11e1a300
    movlw
           0x11
    movwf
           TEMP LOCATION
                                         == 60/0.2X(10e-6)
    movlw
           0xE1
    movwf
           TEMP LOCATION+1
           0xA3
    movlw
    movwf
            TEMP_LOCATION+2
    clrf
            TEMP LOCATION+3
continue subtraction
    movlw
           HIGH (TEMP_LOCATION+3)
    movwf
            FSR0H
           LOW(TEMP_LOCATION+3)
    movlw
    movwf
           FSR0L
    movlw
           HIGH(TEMP_RPM+2)
```

```
movwf
            FSR1H
            LOW (TEMP RPM+2)
    movlw
    movwf
           FSR1L
          STATUS, C
    bsf
          POSTDEC1,W
    movf
    subwfb POSTDECO, F
          POSTDEC1, W
    subwfb POSTDECO, F
           POSTDEC1,W
    movf
    subwfb
            POSTDECO, F
    movlw
            0x0
    subwfb
           POSTDEC0, F
    btfss
           STATUS, C
          keep_result_in_rpm
    goto
           TEMP, F
    incf
    btfsc STATUS, C
    incf
          TEMP1,F
    goto
          continue_subtraction
keep_result_in_rpm
;Timer0 value = FFFF-Timer0
   bsf
           STATUS, C
    movlw
           0xFF
           TEMP,F
    subfwb
    subfwb
           TEMP1,F
    movff TEMP1, SPEED REF H
    movff TEMP, SPEED REF L
    return
;Displays the direction of motor on the host PC screen
display motor direction
    movlw
           0x0
          MOTOR_DIRECTION
    cpfseq
           display_reverse_dir
    goto
           MOTOR RUN FORWARD
    call
    return
display_reverse_dir
          MOTOR_RUN_REVERSE
   call
   return
;This routine intializes the USART module to communicate with host PC and displays
;a welcome message on the screen
STEPPER COM
   movlw
            0xA
   movwf
            TEMP
repeat_send
    incf
            TEMP, F
    incf
            TEMP, F
    movlw
            0x62
    cpfseq
            TEMP
    goto
            send
    return
send
    movf
            TEMP,W
    call
            send_welcome
            load_RX_REG_from_WREG
    call
    goto
            repeat_send
send welcome
    bcf
            STATUS, C
    addwf
            PCL,W
            TEMP1
    movwf
    movlw
            0x0
    addwfc
            PCLATH, F
```

```
addwfc
            PCLATU, F
    movff
            TEMP1, PCL
            'W'
    retlw
           'e'
    retlw
           111
    retlw
    retlw
            'c'
    retlw
            101
            ′ m ′
    retlw
            'e'
    retlw
            , ,
    retlw
            't'
    retlw
            'o'
    retlw
            , ,
    retlw
           'M'
    retlw
            'i'
    retlw
            ' C '
    retlw
            'r'
    retlw
            'o'
    retlw
    retlw
            's'
            't'
    retlw
    retlw
            'e'
    retlw
            'p'
            'p'
    retlw
            'i'
    retlw
    retlw
            'n
    retlw
            ′g′
    retlw
            101
    retlw
            'f'
    retlw
    retlw
            , ,
    retlw
            'S'
           ′t′
    retlw
           'e'
    retlw
           'p'
    retlw
            'p'
    retlw
    retlw
            'e'
    retlw
            'r'
    retlw
    retlw
            ' M'
    retlw
            'o'
            't'
    retlw
            'o'
    retlw
           'r'
    retlw
    retlw
           0x0A
           0x0D
   retlw
;This routine displays the list commands with their explanation on the host PC screen
;***********************
send command request
    movlw
            0xA
    movwf
            TEMP
repeat_send_com_req
          TEMP, F
    incf
    incf
           TEMP, F
    movlw
           0x4A
    cpfseq TEMP
    goto
            send_com_req
    call
            show_commands
    return
send com req
    movf
            TEMP, W
    call
            send_com_request
            load RX REG from WREG
    call
    goto
            repeat_send_com_req
send_com_request
```

```
bcf
              STATUS, C
     addwf
              PCL,W
              TEMP1
    movwf
    movlw
              0x0
              PCLATH, F
    addwfc
    addwfc
              PCLATU, F
    movff
              TEMP1, PCL
    retlw
              0x0A
    retlw
              0x0A
    retlw
              0x0D
    retlw
              'E'
     retlw
              'n'
              't'
    retlw
              'e'
    retlw
              'r'
    retlw
              , ,
    retlw
              't′
     retlw
              'h'
     retlw
    retlw
              'e'
    retlw
              , ,
              'r'
     retlw
     retlw
               'e'
               ′q′
     retlw
              'u'
    retlw
    retlw
              'i'
    retlw
              'e'
    retlw
              'd'
    retlw
              , ,
    retlw
    retlw
              'c'
    retlw
              101
              ′ m ′
    retlw
              ′ m ′
    retlw
              'a'
    retlw
    retlw
            'n'
    retlw
              'd'
    retlw
              0x0A
    retlw
              0x0D
;-----
show commands
    movlw
              0xA
              TEMP
    movwf
repeat_show_com
              TEMP, F
    incf
    incf
              TEMP,F
    movlw
              0xF6
              TEMP
    cpfseq
              show_com
    goto
    return
show com
              TEMP,W
    {\tt movf}
    call
              show_command
              load_RX_REG_from_WREG
    call
    goto
              repeat_show_com
show_command
              STATUS, C
    bcf
    addwf
              PCL,W
    movwf
              TEMP1
    movlw
              0x0
     addwfc
              PCLATH, F
     addwfc
              PCLATU, F
    movff
              TEMP1, PCL
              0x0A
    retlw
    retlw
              0x0D
     retlw
              ' O '
```

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```
retlw
         ' _ '
retlw
        'E'
retlw
        'x'
retlw
        'i'
retlw
        't'
retlw
         , ,
retlw
         'f'
retlw
retlw
         'r'
         'o'
retlw
retlw
         ' m '
         , ,
retlw
        'S'
retlw
        'e'
retlw
        't'
retlw
        'u'
retlw
         'p'
retlw
retlw
         0x0A
retlw
         0x0D
retlw
         11'
         ′ _ ′
retlw
        ′ _ ′
retlw
        'N'
retlw
retlw
        'u'
retlw
         ′ m ′
         'e'
retlw
         'r'
retlw
         , ,
retlw
retlw
         0'
retlw
         ′f′
        , ,
retlw
        'M'
retlw
        'i'
retlw
        'c'
retlw
retlw
        'r'
         'o'
retlw
         's'
retlw
         't'
retlw
retlw
         'e'
         'p'
retlw
         's'
retlw
        '/'
retlw
        's'
retlw
retlw
        't′
         'e'
retlw
         'p'
retlw
retlw
         0x0A
retlw
         0x0D
         121
retlw
        ′ _ ′
retlw
        ′ _ ′
retlw
retlw
        'D'
retlw
        'i'
         'r'
retlw
retlw
         'e'
retlw
         'c'
retlw
         't'
retlw
         'i'
         'o'
retlw
         'n'
retlw
        , ,
retlw
         'o'
retlw
         'f'
retlw
```

```
retlw
    retlw
             'o'
    retlw
             't′
    retlw
             'a'
    retlw
    retlw
    retlw
             'i'
             'o'
    retlw
             'n'
    retlw
    retlw
             0x0A
    retlw
             0x0D
    retlw
             131
             ′ _ ′
    retlw
             ′ _ ′
    retlw
    retlw
             'N'
    retlw
             'u'
    retlw
             ′ m ′
    retlw
             'e'
    retlw
             'r'
              , ,
    retlw
             'o'
    retlw
             'f'
    retlw
    retlw
    retlw
             'S'
    retlw
             't′
             'e'
    retlw
    retlw
             'p'
    retlw
             's'
    retlw
             , ,
    retlw
             't′
             'o'
    retlw
             , ,
    retlw
             'I'
    retlw
    retlw
             'n'
    retlw
             'c'
    retlw
             'h'
    retlw
             0x0A
    retlw
             0x0D
    retlw
             44'
              , _ ,
    retlw
             ′ _ ′
    retlw
             'S'
    retlw
    retlw
             'e'
    retlw
             ′t′
             , ,
    retlw
    retlw
             'R'
    retlw
             'P'
    retlw
              'M'
    retlw
             0x0A
    retlw
             0x0A
    retlw
             0x0D
;This routine displays message during exit from the PC communication on the host PC screen
DISPLAY_EXIT_SETUP
    movlw
             0xA
    movwf
             TEMP
repeat_show_exit
    incf
             TEMP,F
    incf
             TEMP, F
    movlw
             0x2E
```

```
cpfseq
            TEMP
    goto
            show exit
    return
show_exit
            TEMP,W
    movf
            show_exit_setup
    call
    call
           load RX REG from WREG
    goto
           repeat_show_exit
show_exit_setup
    bcf
            STATUS, C
    addwf
            PCL,W
    movwf
            TEMP1
    movlw
           0x0
    addwfc PCLATH, F
    addwfc PCLATU, F
    movff
            TEMP1, PCL
    retlw
            0x0A
    retlw
            0x0D
    retlw
            'B'
            'Y'
    retlw
    retlw
            'E'
            , ,
    retlw
            'B'
    retlw
            'Y'
    retlw
    retlw
            'E'
            , ,
    retlw
            ′.′
    retlw
            '.'
    retlw
            '.'
    retlw
    retlw
            ' . '
            ′.′
    retlw
    retlw
            0x0A
    retlw
            0x0D
;This routine displays the list of data available for microstep selection on the host PC screen
DISPLAY_STEPS_VALUE
    movlw
            0xA
    movwf
            TEMP
repeat_show_step_value
          TEMP,F
    incf
    incf
            TEMP, F
    movlw
           0xD0
    cpfseq TEMP
    goto
            show_step
    return
show_step
    movf
            TEMP,W
    call
            show step command
    call
            load RX REG from WREG
            repeat_show_step_value
    goto
show_step_command
    bcf
            STATUS, C
    addwf
           PCL,W
           TEMP1
    movwf
    movlw
            0x0
    addwfc
            PCLATH, F
    addwfc
            PCLATU, F
    movff
            TEMP1, PCL
    retlw
            0x0A
    retlw
            0x0D
            'E'
    retlw
    retlw
            'n
            't'
    retlw
```

```
retlw
         'e'
retlw
         'r'
retlw
         't′
retlw
         'h'
retlw
retlw
         'e'
         , ,
retlw
         'N'
retlw
         0'
retlw
         ′.′
retlw
         , ,
retlw
         ′ 0 ′
retlw
        'f'
retlw
        , ,
retlw
       ′ M ′
retlw
retlw
       'i'
       'c'
retlw
         'r'
retlw
retlw
         101
retlw
         's'
         ′t′
retlw
         'e'
retlw
         'p'
retlw
         's'
retlw
retlw
         0x0A
retlw
         0x0D
retlw
         'E'
retlw
         'n
         't′
retlw
retlw
         'e'
         'r'
retlw
        , ,
retlw
        111
retlw
       , ,
retlw
       'f'
retlw
retlw
       ′ 0 ′
retlw
         'r'
         , ,
retlw
         111
retlw
retlw
         121
retlw
         , ,
retlw
         'f'
retlw
         101
retlw
retlw
       'r'
         , ,
retlw
         121
retlw
         ′,′
retlw
retlw
         131
        , ,
retlw
        'f'
retlw
        'o'
retlw
       'r'
retlw
retlw
        , ,
retlw
       ' 4 '
retlw
         ' 4 '
retlw
retlw
         ′f′
retlw
retlw
         0'
         'r'
retlw
retlw
         181
retlw
retlw
         151
retlw
```

```
retlw
    retlw
             ′f′
              'o'
    retlw
              'r'
    retlw
    retlw
    retlw
             111
    retlw
             '6'
             , ,
    retlw
             'a'
    retlw
    retlw
             'n'
             'd'
    retlw
             , ,
    retlw
             161
    retlw
             , ,
    retlw
             'f'
    retlw
    retlw
             'o'
    retlw
    retlw
             131
    retlw
              121
    retlw
              , ,
    retlw
             's'
    retlw
             't'
    retlw
             'e'
    retlw
    retlw
             'p'
    retlw
             's'
    retlw
             0x0A
    retlw
             0x0D
;This routine displays the selction of motor direction on the host PC screen
DISPLAY STEPS DIRECTION
    movlw
             0xA
    movwf
             TEMP
repeat show step direction
    incf
            TEMP, F
            TEMP, F
    incf
    movlw
             0x7C
    cpfseq
             TEMP
    goto
             show_step_direction
    return
show_step_direction
    movf
            TEMP, W
    call
             show step direction values
    call
            load_RX_REG_from_WREG
    goto
            repeat_show_step_direction
show_step_direction_values
    bcf
             STATUS, C
    addwf
             PCL,W
    movwf
             TEMP1
            0x0
    movlw
    addwfc PCLATH, F
    addwfc PCLATU, F
    movff
             TEMP1, PCL
    retlw
             0x0A
    retlw
             0x0D
    retlw
             'E'
    retlw
              'n'
    retlw
             't′
    retlw
             'e'
             'r'
    retlw
             , ,
    retlw
    retlw
             'D'
             'i'
    retlw
```

```
retlw
            'r'
    retlw
            'e'
            'c'
    retlw
            't'
    retlw
            'i'
    retlw
    retlw
            101
    retlw
            'n
            , ,
    retlw
            101
    retlw
            'f'
    retlw
            , ,
    retlw
            'r'
    retlw
            ′0′
    retlw
            't′
    retlw
            'a'
    retlw
    retlw
            't′
            'a'
    retlw
            'i'
    retlw
    retlw
            101
    retlw
            'n'
    retlw
            0x0A
    retlw
            0x0D
            ′ O ′
    retlw
    retlw
    retlw
            ′ _ ′
    retlw
            'F'
    retlw
            101
            'r'
    retlw
            'w'
    retlw
    retlw
            'a'
    retlw
            'r'
            'd'
    retlw
    retlw
            0x0A
    retlw
            0x0D
            111
    retlw
    retlw
            ′ _ ′
            ' - '
    retlw
    retlw
            'R'
    retlw
            'e'
    retlw
            'v'
            'e'
    retlw
            'r'
    retlw
    retlw
            's'
            'e'
    retlw
    retlw
            0x0A
    retlw
            0x0D
; This routine displays the message for INCH command on the host PC screen
DISPLAY STEPS INCH
            0xA
    movlw
            TEMP
    movwf
repeat show step inch
    incf
           TEMP, F
    incf
            TEMP, F
    movlw
            0x4E
            TEMP
    cpfseq
    goto
            show_step_inch
    return
show_step_inch
            TEMP,W
    movf
            show_step_inch_values
    call
    call
            load_RX_REG_from_WREG
    goto
            repeat_show_step_inch
```

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```
show_step_inch_values
    bcf
           STATUS, C
    addwf
            PCL,W
          TEMP1
    movwf
    movlw
           0x0
    addwfc PCLATH, F
    addwfc PCLATU, F
    movff TEMP1, PCL
            0x0A
    retlw
    retlw
            0x0D
            'E'
    retlw
            'n'
    retlw
           ′t′
    retlw
           'e'
    retlw
            'r'
    retlw
            , ,
    retlw
            'N'
    retlw
            'u'
    retlw
    retlw
            ' m '
            'b'
    retlw
    retlw
            'e'
    retlw
            'r'
    retlw
            101
    retlw
    retlw
            'f'
            , ,
    retlw
            's'
    retlw
            't'
    retlw
    retlw
            'e'
    retlw
            'p'
    retlw
            's'
            , ,
    retlw
           't′
    retlw
           101
    retlw
            , ,
    retlw
    retlw
            'I'
    retlw
            'N'
            ′ C′
    retlw
    retlw
            'H'
    retlw
            0x0A
    retlw
            0x0D
; This routine displays the message for RPM command on the host PC screen
DISPLAY_STEPS_RPM
    movlw 0xA
    movwf
            TEMP
repeat show step rpm
         TEMP, F
    incf
    incf
            TEMP, F
           0x40
    movlw
    cpfseq TEMP
    goto
            show_step_rpm
    return
show_step_rpm
         TEMP,W
    movf
         show_step_rpm_values load_RX_REG_from_WREG
    call
    call
    goto
            repeat_show_step_rpm
show_step_rpm_values
    bcf
            STATUS, C
    addwf
            PCL,W
    movwf
            TEMP1
    movlw
            0x0
```

```
addwfc
             PCLATH, F
    addwfc
             PCLATU, F
    movff
             TEMP1, PCL
    retlw
             0x0A
             0x0D
    retlw
    retlw
    retlw
             'n
             ′t′
    retlw
             'e'
    retlw
    retlw
             'r'
             , ,
    retlw
             't'
    retlw
             'h'
    retlw
             'e'
    retlw
    retlw
    retlw
             'r'
    retlw
             'e'
             'q'
    retlw
    retlw
             'u'
             'i'
    retlw
    retlw
             'r'
    retlw
             'e'
             'd'
    retlw
    retlw
    retlw
             'R'
    retlw
             'P'
    retlw
             'M'
    retlw
             0x0A
    retlw
             0x0D
;This routine displays the message Motor running Forward on the host PC screen
MOTOR RUN FORWARD
    movlw
             0xA
    movwf
             TEMP
repeat show motor fwd
    incf
            TEMP, F
             TEMP, F
    incf
    movlw
             0x3E
    cpfseq
             TEMP
    goto
             show_fwd_running
    return
show fwd running
    movf
             TEMP, W
    call
             show forward running
    call
            load_RX_REG_from_WREG
    goto
             repeat_show_motor_fwd
show_forward_running
    bcf
             STATUS, C
    addwf
             PCL,W
    movwf
             TEMP1
    movlw
             0x0
             PCLATH, F
    addwfc
    addwfc
             PCLATU, F
    movff
             TEMP1, PCL
    retlw
             0x0A
    retlw
             0x0D
    retlw
             ' M '
              'o'
    retlw
    retlw
             ′t′
    retlw
             'o'
             'r'
    retlw
    retlw
    retlw
             'R'
    retlw
             'u'
```

```
retlw
                                                                              'n'
                          retlw
                                                                              'n'
                                                                             'i'
                         retlw
                                                                             'n'
                         retlw
                                                                             ′g′
                         retlw
                         retlw
                         retlw
                                                                            'F'
                                                                            0'
                         retlw
                                                                             'r'
                         retlw
                         retlw
                                                                             'w'
                                                                             'a'
                         retlw
                         retlw
                                                                             'r'
                                                                             'd'
                         retlw
                                                                             0x0A
                         retlw
                         retlw
                                                                             0x0D
 ; This routine displays the message Motor running Reverse on the host PC screen % \left( 1\right) =\left( 1\right) +\left( 1\right) +\left(
MOTOR RUN REVERSE
                         movlw
                                                                             0xA
                         movwf
                                                                            TEMP
repeat_show_motor_rev
                                                                     TEMP, F
                         incf
                         incf
                                                                            TEMP, F
                         movlw
                                                                     0x3E
                         cpfseq
                                                                TEMP
                                                                         show_rev_running
                         goto
                         return
show_rev_running
                         movf
                                                                            TEMP, W
                         call
                                                                           show_reverse_running
                                                                       load_RX_REG_from_WREG
                         call
                         goto
                                                                     repeat_show_motor_rev
show_reverse_running
                         bcf
                                                                     STATUS, C
                         addwf
                                                                         PCL,W
                                                                           TEMP1
                         movwf
                         movlw
                                                                            0x0
                         addwfc
                                                                             PCLATH, F
                         addwfc
                                                                             PCLATU, F
                         movff
                                                                            TEMP1, PCL
                         retlw
                                                                            0x0A
                         retlw
                                                                            0x0D
                         retlw
                                                                            'M'
                                                                            'o'
                         retlw
                                                                           't′
                         retlw
                         retlw
                                                                             'o'
                         retlw
                                                                             'r'
                                                                             , ,
                         retlw
                                                                             'R'
                         retlw
                                                                              'u'
                         retlw
                                                                             'n'
                         retlw
                         retlw
                                                                             'n'
                         retlw
                                                                             'i'
                                                                             'n'
                         retlw
                                                                             ′g′
                         retlw
                         retlw
                         retlw
                                                                             'R'
                          retlw
                                                                              'e'
                                                                             'v'
                         retlw
                                                                             'e'
                         retlw
                                                                             'r'
                         retlw
                         retlw
                                                                             's'
                          retlw
```

```
retlw
             0x0A
    retlw
             0x0D
; This routine displays the message Data not valid on the host PC screen
DATA NOT VALID
    movlw
             0xA
    movwf
             TEMP
repeat_show_data_not_valid
    incf
             TEMP, F
    incf
             TEMP, F
    movlw
             0x3E
    cpfseq
             TEMP
    goto
             show_data_not_valid
    return
show_data_not_valid
    movf
            TEMP,W
             show_not_valid_data
    call
    call
           load_RX_REG_from_WREG
    goto
             repeat_show_data_not_valid
show_not_valid_data
    bcf
             STATUS, C
    addwf
             PCL,W
    movwf
             TEMP1
    movlw
             0x0
    addwfc
             PCLATH, F
    addwfc
             PCLATU, F
    movff
             TEMP1, PCL
             0x0A
    retlw
    retlw
             0x0D
    retlw
             'N'
             101
    retlw
             'T'
    retlw
             , ,
    retlw
    retlw
             'A'
             , ,
    retlw
             'V'
    retlw
    retlw
             'A'
    retlw
             'L'
    retlw
             'I'
             'D'
    retlw
             , ,
    retlw
    retlw
             'E'
             'N'
    retlw
    retlw
             'T'
    retlw
             'R'
             'Y'
    retlw
    retlw
             ′.′
    retlw
             ' . '
    retlw
             ' . '
    retlw
    retlw
             0x0A
             0x0D
    retlw
    end
```



NOTES:

Note the following details of the code protection feature on PICmicro® MCUs.

- The PICmicro family meets the specifications contained in the Microchip Data Sheet.
- Microchip believes that its family of PICmicro microcontrollers is one of the most secure products of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the PICmicro microcontroller in a manner outside the operating specifications contained in the data sheet. The person doing so may be engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not
 mean that we are guaranteeing the product as "unbreakable".
- Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our product.

If you have any further questions about this matter, please contact the local sales office nearest to you.

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Microchip received QS-9000 quality system certification for its worldwide headquarters, design and wafer fabrication facilities in Chandler and Tempe, Arizona in July 1999. The Company's quality system processes and procedures are QS-9000 compliant for its PICmicro® 8-bit MCUs, KEELOQ® code hopping devices, Serial EEPROMs and microperipheral products. In addition, Microchip's quality system for the design and manufacture of development systems is ISO 9001 certified.



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