



MIL/AERO PART NOMENCLATURE GUIDE

NSID: XXXX XXXX NNNN AAA XXXX XXXXX

A. PREFIX/FAMILY

JD	JAN DIGITAL
JL	JAN LINEAR
JM	JAN MOS

LOGIC

9xx	DTL LOGIC
54xx	MILITARY LOGIC
74xx	COMMERCIAL LOGIC
93xx, 96xx	MILITARY TTL LOGIC
100xxx	F100K 300 SERIES ECL
CD4xxx	CD4000 SERIES
CGS	CLOCK GENERATED SUPPORT
DM	DIGITAL MONOLITHIC
MM	CMOS
NS	SRAM
SCAN	SCAN SYSTEM AND BOARD TEST

F. SUFFIX #2: 1-5 CHARACTERS

E. PACKAGE: 1-4 CHARACTERS

D. SUFFIX #1: 1-3 LETTERS

C. DEVICE: 2-5 NUMBERS

B. PROCESS: 1-4 CHARACTERS

A. FAMILY: 2-4 CHARACTERS

ANALOG

ADC	ANALOG TO DIGITAL CONVERTER (DATA ACQUISITION)
CLC	COMLINEAR FUNCTIONS
DAC	DIGITAL TO ANALOG CONVERTER (DATA ACQUISITION)
LF	LINEAR BIFET (AMP, BUFFER, COMPARATOR OR DATA ACQ)
LH	LINEAR HYBRID (AMP, BUFFER, COMPARATOR OR VOLT REF)
LM	LINEAR MONOLITHIC (ALL ANALOG FAMILIES)
LMC	LINEAR CMOS (OP AMP)
LMD	LINEAR MONOLITHIC DMOS (MOTOR CONTROL FOR DATA ACQ)
LMF	LINEAR FILTER (SWITCHED CAP FILTER FOR DATA ACQ)
LMX	PLL
LP	LINEAR LOW POWER
LPC	LINEAR LOW POWER CMOS (LOW POWER OP AMP)
OP	60 MICRO VOLT OP AMP

INTERFACE

DP	INTERFACE MICROPROCESSOR PERIPHERALS
DS	INTERFACE (TRANSMISSION DRIVERS/RECEIVERS)

OTHER

AR	ARINC (AVIONICS)
COP	CONTROL ORIENTED PROCESSOR
HPC	HI PERFORMANCE CONTROLLER
NSC8	NSC 800 MICRO PROCESSOR

NOTE: EACH NSID CAN HAVE MORE THAN ONE PREFIX/FAMILY

B. INTERNAL PROCESS IDENTIFICATION CODE

FACT LOGIC

AC	FACT
ACT	FACT (TTL COMPATIBLE)
ACQ	FACT QUIET SERIES
ACTQ	FACT QUIET SERIES (TTL COMPATIBLE)
FCT	HI SPEED CMOS FACT

OTHER CMOS

ABT	ADVANCED BIPOLAR CMOS
C	CMOS
HC	HIGH SPEED CMOS
HCT	HIGH SPEED CMOS (TTL COMPATIBLE)

BIPOLAR

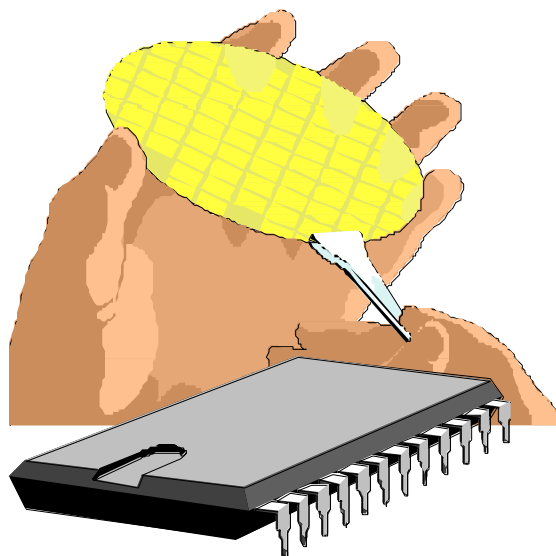
L	LOW POWER TTL
LS	LOW POWER SCHOTTKY
F	FAIRCHILD ADVANCED SCHOTTKY (FAST)

INTERFACE (DP/DS)

14,16,26,55,	RS-232, 422, 423 AND 485 DRIVERS & RECEIVERS
75,78,96	RS-232, 422, 423 AND 485 DRIVERS & RECEIVERS
90	LVDS

DATA ACQUISITION (ADC/DAC)

ADCnn...	nn = # OF BITS
DACnn...	nn = # OF BITS



C. DEVICE NUMBER

LOGIC (NOTE: 93, 96, F100K AND CD4K DO NOT FOLLOW THESE DEVICE #'S)

n0	NAND	GATE
02	NOR	GATE
03	NAND - OPEN COLLECTOR	GATE
04	HEX INVERTER	GATE
05, 06, 07, 17	HEX INVERTER/BUFFER - OPEN D/C	OPEN D/C
n1, 08, 15	AND	GATE
09, 16	AND - OPEN COLLECTOR	OPEN D/C
13, 14	SCHMITT TRIGGER	TRIGGER
32	OR	GATE
38	NAND BUFFER - OPEN COLLECTOR	OPEN D/C
42	BCD TO DECIMAL DEC	ARITHMETIC
64	AND/OR INVERT GATE	GATE
73	JK FLIP-FLOP	FLIP-FLOP
86	EXCLUSIVE OR	GATE
93, 19n	COUNTER	ARITHMETIC
95, 194, 195	SHIFT REGISTER	ARITHMETIC
109, 112	DUAL JK FLIP-FLOP	FLIP-FLOP
121, 122	MULTIVIBRATOR	ONE SHOT
125	QUAD BUFFER	BUFFER/TRANSC
132	NAND SCHMITT TRIGGER	TRIGGER
13n	DECODER/DEMULTIPLEXER	DECODE/ENCODE
164	SHIFT REGISTER	REGISTER
165	PARALLEL-TO-SERIAL CONVERTER	ARITHMETIC
16n	COUNTER	ARITHMETIC
181	ARITHMETIC LOGIC UNIT	ARITHMETIC
182	CARRY LOOKAHEAD GENERATOR	ARITHMETIC
189, 219	64-BIT RAM	
273, 82n	D FLIP FLOP	FLIP-FLOP
280	PARITY GENERATOR/CHECKER	SYSTEM TEST
283	FULL ADDER	ARITHMETIC
322, 323	OCTAL SHIFT REGISTER	REGISTER
365	HEX BUFFER	BUFFER/TRANSC
378, 379	D REGISTER	REGISTER
402	POLYNOMIAL GENERATOR/CHECKER	SYSTEM TEST
407	DATA ACCESS REGISTER	REGISTER
52n	8-BIT COMPARATOR	COMPARATOR
533	TRANSPARENT LATCH	LATCH
534	OCTAL D FLIP-FLOP	FLIP-FLOP
563	OCTAL D LATCH	LATCH
56n	OCTAL D INVERTING FLIP-FLOP	FLIP-FLOP
651, 652	OCTAL TRANSCEIVER/REGISTER	BUFFER/TRANSC
657	TRANSCEIVER WITH PARITY	SYSTEM TEST
670	REGISTER FILE	REGISTER
676	16-BIT SHIFT REGISTER	REGISTER
715	VIDEO SYNC GENERATOR	CLOCK/TIMING
818	DIAGNOSTIC AND PIPELINE REGISTER	SYSTEM TEST
827	BUFFER/DRIVER	BUFFER/TRANSC
841	TRANSPARENT LATCH	LATCH
899	TRANSCEIVER GENERATOR/CHECKER	SYSTEM TEST
90n	HEX TTL BUFFER	TRANSLATOR
922	KEYBOARD ENCODER	DECODE/ENCODE
n4n, nn4n, nnn4n	BUFFER/DRIVER OR TRANSCEIVER	BUFFER/TRANSC
n5n	MULTIPLEXER	MULTIPLEXER
n73, n79	LATCH	LATCH
, n, nnn74/75/77	D FLIP-FLOP	FLIP-FLOP
n98, n99	REGISTER	REGISTER
2525	MIN SKEW CLOCK DRIVER	CLOCK/TIMING
16500	16-BIT UNIVERSAL TRANSCEIVER	BUFFER/TRANSC
16nnn, 18nnn	16-BIT/18-BIT WIDE LOGIC	

D. NSC SUFFIX #1 (BEFORE PACKAGE CODE)

NOTE: EACH NSID CAN HAVE MORE THAN ONE SUFFIX

A, B, C, etc.	IMPROVED/REVISED DEVICE OR SPEED/PERFORMANCE GRADE
AD, ADJ	ADJUSTABLE OUTPUT VOLTAGE
C	COMMERCIAL TEMP RANGE
L	LOW CURRENT
HV	HIGH VOLTAGE
I	INDUSTRIAL TEMP RANGE
M	MILITARY TEMP RANGE

PRODUCT ASSURANCE DESIGNATORS

B,M,Q	CLASS B
S,V	CLASS S

E. PACKAGE CODE

NSC MIL/AERO

D	GLASS/METAL DIP
DM	CERAMIC DIP
E	LEADLESS CERAMIC CHIP CARRIER
EL	LEADED CERAMIC CHIP CARRIER
F	GLASS/METAL FLATPAK
FM	FLATPAK
G	12 LEAD TO-8 METAL CAN
H	MULTI-LEAD METAL CAN
J _n	LO-TEMP CERAMIC DIP (n = LEAD COUNT)
K	TO-3 METAL CAN IN STEEL/ALUMINUM
KSTEEL	TO-3 METAL CAN (STEEL)
L	CERAMIC LEADED CHIP CARRIER
LM	CERAMIC LEADLESS CHIP CARRIER
Q	CERAMIC DIP WITH UV WINDOW
U	PIN GRID ARRAY
W	LO-TEMP CERAMIC FLATPAK
WG	GULL WING CERAMIC SOIC
Y	CERAMIC LEADLESS CHIP CARRIER

KNOWN GOOD DIE PRODUCTS

MDC,MD8,MDCT,MD8T	OPTION 1: DC PROBE
MDS	OPTION 2: S-LEVEL DC PROBE WITH LAT
MDA,M3S,MWA,MDAT	OPTION 3: AC/DC PROBE AT WORST CASE TEMP
MDQ	OPTION 4: 100% DIE-LEVEL TEST @ TEMP & BURN-IN

JAN/SMD PACKAGE CODES

C	14-PIN CDIP
D	14-PIN FLATPAK
E	16-PIN CDIP
F	16-PIN FLATPAK
G	8-PIN TO-99 CAN
H	10-PIN FLATPAK
I	10-PIN TO-100 CAN
K	24-PIN FLATPAK
L	24-PIN CDIP
P	8-PIN CDIP
R	20-PIN CDIP
S	24-PIN FLATPAK
X	NON-STANDARD PACKAGE
Y	NON-STANDARD PACKAGE
2	20-PIN LCC
3	28-PIN LCC

CLC PACKAGE CODES

B	CERAMIC/SOLDER DIP
D	SIDEBRAZED DIP/GOLD
F	FLATPAK/GOLD
H	CERPAK/SOLDER

F. NSC SUFFIX #2 (AFTER PACKAGE CODE)

JAN/SMD LEAD FINISH SUFFIXES

A	SOLDER TIN
B	
C	GOLD
X	ANY FINISH